

MiniSKiiP® 1

Twin 6-pack

SKiiP 12ACC12T4V10

Features

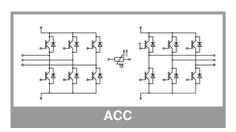
- Trench 4 IGBTs
- Robust and soft freewheeling diodes in CAL technology
- Highly reliable spring contacts for electrical connections
- UL recognised: File no. E63532

Typical Applications*

• 4Q inverters

Remarks

- Max. case temperature limited to T_C=125°C
- Terminal distances sufficient for basic insulation in 3-phase 480VAC TN systems
- DC-link voltage V_{DC}≤800V
- Max. 500V potential difference between +rect and +DC
- Max. 500V potential difference between -rect and -DC
- Temperature sensor: no basic insulation to main circuit, signal processing with reference to -DC potential
- Please refer to MiniSKiiP "Technical Explanations" and "Mounting Instructions" for further information



200 18 15 19 16 8 24 20 10 175 200 28 23 31 26 15 45	V A A A A A A A A A A A A A A A A A A A
200 18 15 19 16 8 24 20 10 175 200 28 23 31 26 15	V A A A A A V μs °C
18 15 19 16 8 24 20 10 175 200 28 23 31 26 15	A A A A A V μs °C
18 15 19 16 8 24 20 10 175 200 28 23 31 26 15	A A A A A V μs °C
15 19 16 8 24 20 10 175 200 28 23 31 26 15	A A A A V μs °C
19 16 8 24 20 10 175 200 28 23 31 26 15	A A A V μs °C
16 8 24 20 10 175 200 28 23 31 26 15	A A V μs °C V A A A
8 24 20 10 175 200 28 23 31 26 15	A A V μs °C V A A A A A
24 20 10 175 200 28 23 31 26 15	A V μs °C V A A A A
200 28 23 31 26 15	V μs °C V A A A
10 175 200 28 23 31 26 15	μs °C V A A A A A
175 200 28 23 31 26 15	°C V A A A
175 200 28 23 31 26 15	°C V A A A
200 28 23 31 26	V A A A
28 23 31 26 15	A A A
28 23 31 26 15	A A A
23 31 26 15	A A A
31 26 15	A A
26 15	Α
15	
	Α
45	
	Α
20	V
10	μs
175	°C
200	V
14	Α
11	Α
15	Α
12	Α
8	Α
16	Α
55	Α
150	°C
200	V
	A
	A
	A
	A
	A
	Α Δ
	°C
1/3	U
20	Α
	°C
500	V
	200



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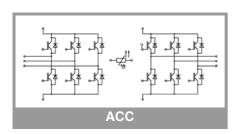
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Symbol Cond	ditions		min.	tvn		1
IGBT 1 - 6				typ.	max.	Unit
$V_{CE(sat)}$ $I_C = 8$	Α	T _i = 25 °C		1.85	2.10	٧
V _{GE} =		T _i = 150 °C		2.25	2.45	V
chiple	evel	1				
V _{CE0} chiplevel	evel	T _j = 25 °C T _i = 150 °C		0.80	0.90	V
	45.1/	$T_i = 150^{\circ} \text{C}$ $T_i = 25^{\circ} \text{C}$			0.80	<u> </u>
	: 15 V -vel			131 194	150 206	mΩ
	chiplevel $T_j = 150 ^{\circ}\text{C}$ $V_{GE} = V_{CE} V, I_C = 1 \text{mA}$		5	5.8	6.5	mΩ
· ' '		T _i = 25 °C	5	0.1	0.3	mA
		1, - 23 0		0.1	0.5	mA
C _{ies}		f = 1 MHz		0.49		nF
$\frac{V_{CE}}{V_{Ces}}$	25 V	f = 1 MHz		0.05		nF
$V_{GE} = V_{GE}$	0 V	f = 1 MHz		0.03		nF
	- 8 V+ 15 V			45		nC
R_{Gint} $T_i = 2$				0.0		Ω
	: 600 V	T _i = 125 °C		117		ns
$\frac{t_{d(OI)}}{t_r}$ $I_C = 8$		T _i = 125 °C		70		ns
E H _{G on}	= 51 Ω	T _i = 125 °C		1		mJ
— □G off	= 51 Ω _n = 97 A/μs	T _i = 125 °C		300		ns
	_{ff} = 106 A/μs	T _i = 125 °C		120		ns
	= 3300 V/µs	-1				1
$V_{GE} = L_s = 2$: +15/-15 V 22 nH	T _j = 125 °C		0.7		mJ
• ,	BT, λ _{paste} =0.8			1.84		K/W
R _{th(j-s)} per IC	BBT, λ_{paste} =2.5	5 W/(mK)		1.6		K/W
IGBT 7 - 12						
$V_{CE(sat)}$ $I_C = 1$		T _j = 25 °C		1.85	2.10	V
chiple	: 15 V evel	T _j = 150 °C		2.25	2.45	V
V _{CE0}	wol	T _j = 25 °C		0.80	0.90	٧
chiple	evei	T _j = 150 °C		0.70	0.80	٧
r _{CE} V _{GE} =	15 V	T _j = 25 °C		70	80	mΩ
chiple	evel	T _j = 150 °C		103	110	mΩ
$V_{GE(th)}$ $V_{GE} =$	$V_{CE} V, I_{C} = 1$		5	5.8	6.5	V
$V_{GE} = 0$		T _j = 25 °C		0.1	0.3	mA
V _{CE} =	1200 V			-		mA
$V_{CE} =$	25 V	f = 1 MHz		0.90		nF
V _{GE} =	0 V	f = 1 MHz		0.08		nF
C _{res}		f = 1 MHz		0.06		nF
	V _{GE} = -8 V+ 15 V			85		nC
	$T_j = 25 ^{\circ}\text{C}$ $V_{CC} = 600 ^{\circ}\text{V}$ $T_i = 150 ^{\circ}\text{C}$			0		Ω
I ₂ = 1		T _j = 150 °C		92		ns
Rc on	$=39 \Omega$	T _j = 150 °C		74		ns
E _{on} R _{G off}	= 39 Ω	T _j = 150 °C		2.1		mJ
	n = 188 A/μs	T _j = 150 °C		319		ns
	_{ff} = 200 A/μs = 3500 V/μs	T _j = 150 °C		77		ns
	+15/-15 V	T _j = 150 °C		1.6		mJ
	BT, λ _{paste} =0.8	8 W/(mK)		1.3		K/W
	per IGBT, λ_{paste} =2.5 W/(mK)			1.1		K/W



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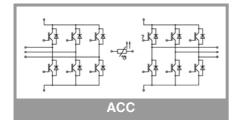
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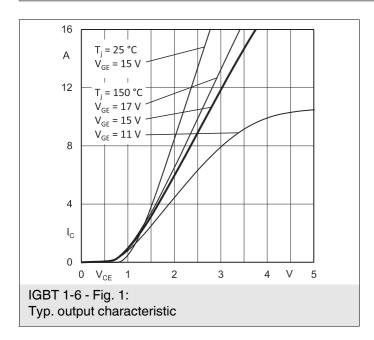
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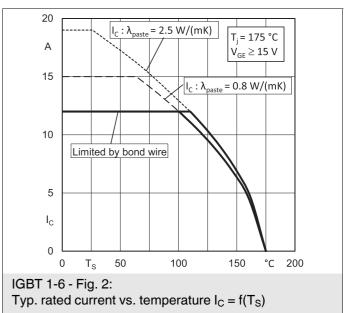
Remarks

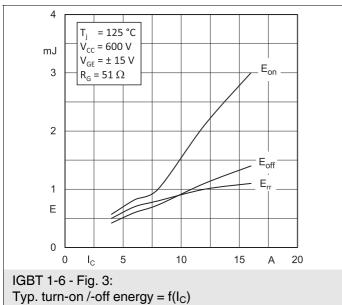
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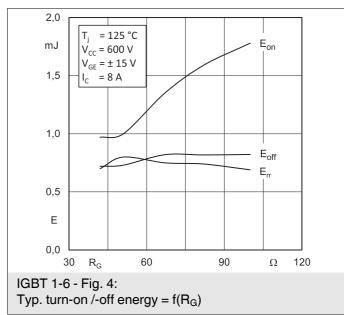
Characte	ristics									
Symbol	Conditions		min.	typ.	max.	Unit				
Diode 1 - 6										
$V_F = V_{EC}$	$V_F = V_{EC}$ $V_{GE} = 0 V$ $Chiplevel$	T _j = 25 °C		1.96	2.22	V				
		T _j = 125 °C		2.08	2.34	V				
V _{F0} chiplevel	chiployol	T _j = 25 °C		1.00	1.10	V				
	Criipievei	T _j = 125 °C		0.80	0.90	V				
r _F	chiplevel	T _j = 25 °C		120	140	mΩ				
		T _j = 125 °C		160	180	mΩ				
I _{RRM}	$I_F = 8 A$ $di/dt_{off} = 93 A/\mu s$	T _j = 125 °C		5.4		Α				
Q_{rr}		T _j = 125 °C		1.9		μC				
E _{rr}	V _{GE} = -15 V V _{CC} = 600 V	T _j = 125 °C		0.8		mJ				
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			2.5		K/W				
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			2.2		K/W				
Diode 7 -	12					•				
$V_F = V_{EC}$	$V_F = V_{EC}$ $V_{GE} = 0 V$ $chiplevel$	T _j = 25 °C		2.38	2.71	V				
		T _j = 150 °C		2.44	2.77	V				
V _{F0} chiplevel	ahinlayal	T _j = 25 °C		1.30	1.50	V				
	T _j = 150 °C		0.90	1.10	V					
r _F	Chiplevel IF = 15 A	T _j = 25 °C		72	81	mΩ				
		T _j = 150 °C		103	111	mΩ				
I _{RRM}		T _j = 150 °C		8.9		Α				
Q _{rr}		T _j = 150 °C		2.2		μC				
E _{rr}	$V_{CC} = 600 \text{ V}$	T _j = 150 °C		0.8		mJ				
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			1.92		K/W				
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			1.7		K/W				
Module										
L _{CE}				60		nH				
Ms	to heat sink	2		2.5	Nm					
w				30		g				
Temperat	ure Sensor									
R ₁₀₀	T _r =100°C (R ₂₅ =1000Ω)			1670 ± 3%		Ω				
R(T)	R(T)=1000Ω[1+A($^{-1}$], A = 7.635*10 ⁻³ °C B = 1.731*10 ⁻⁵ °C									

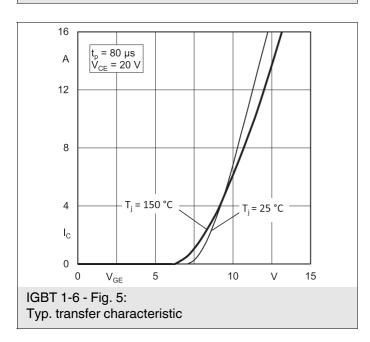


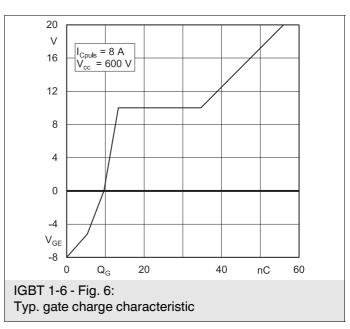


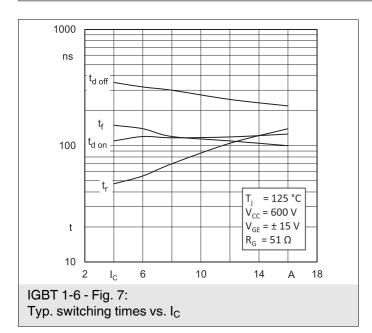


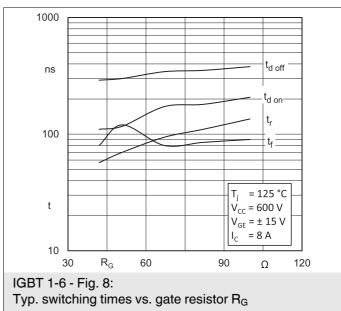


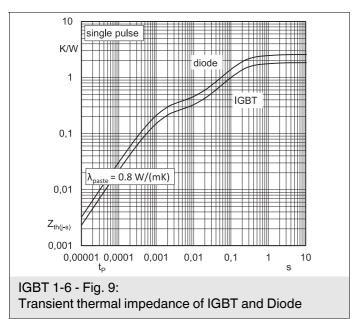


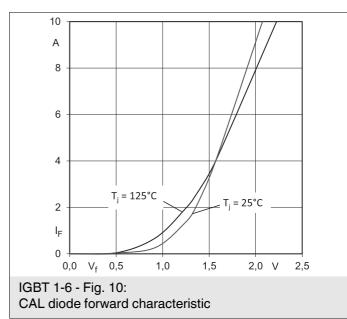


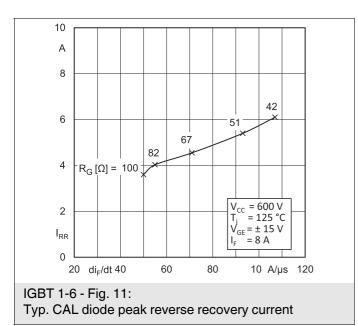


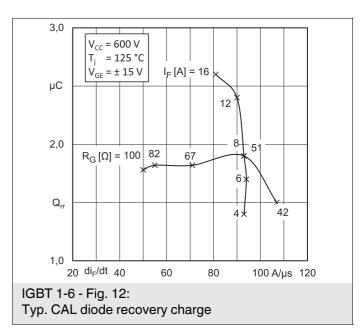


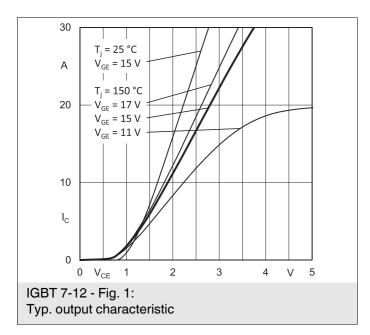


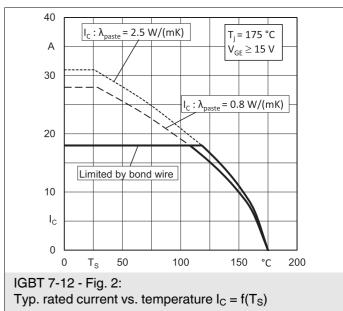


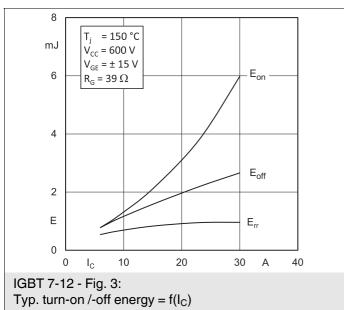


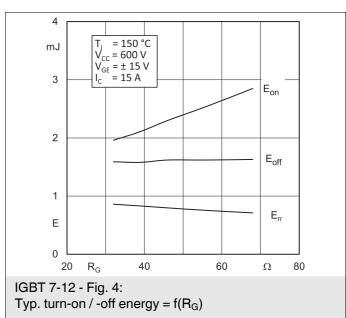


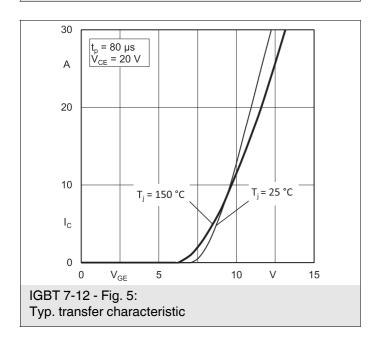


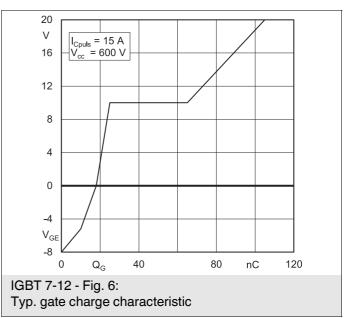


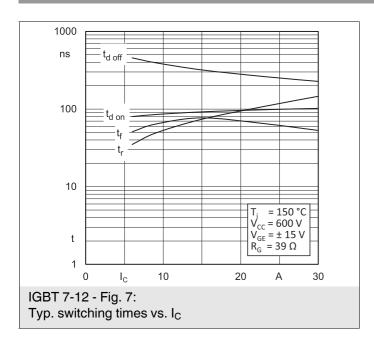


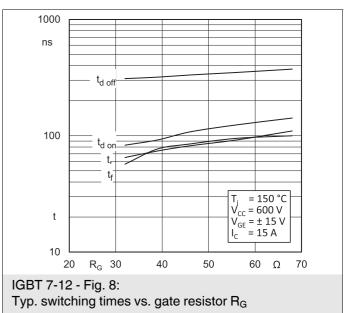


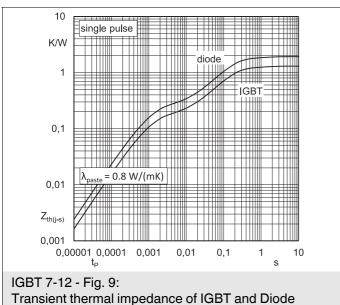


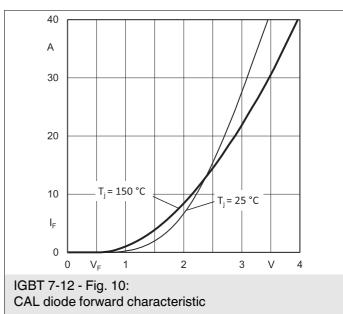


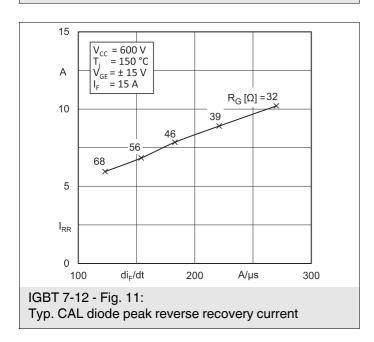


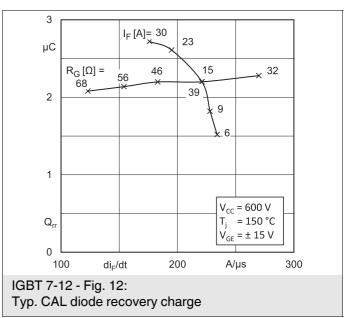


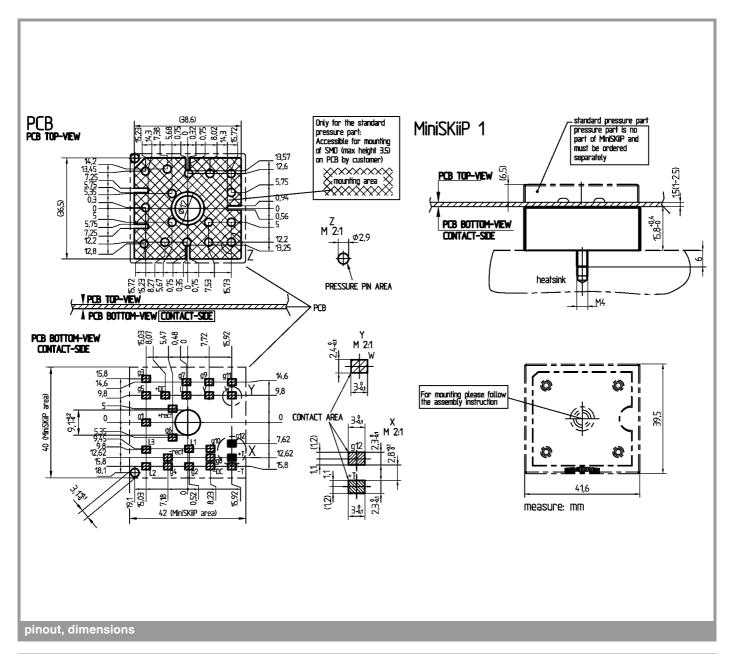


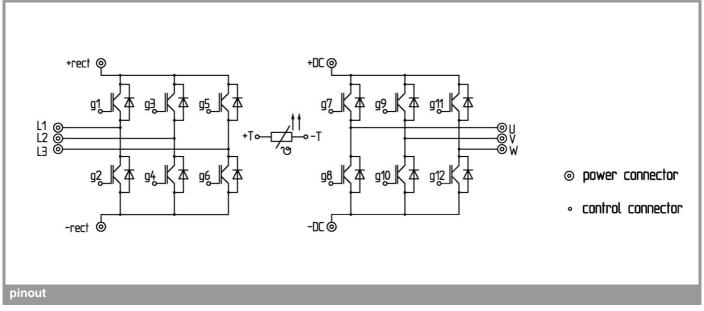












This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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