

# **Surface Mounted Power Resistor Thick Film Technology**

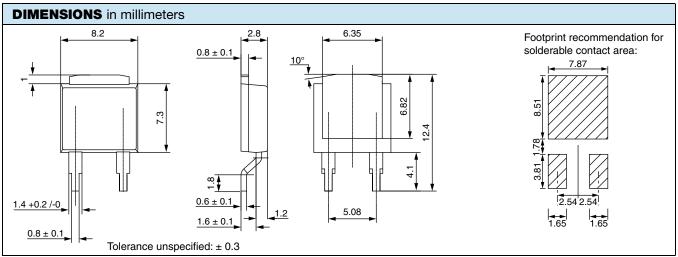


#### **FEATURES**

- AEC-Q200 qualified
- 25 W at 25 °C case temperature



- Wide resistance range: 0.016  $\Omega$  to 700 k $\Omega$
- Non inductive
- · Resistor isolated from metal tab
- Solder reflow secure at 270 °C / 10 s, MSL = 1
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



#### **Notes**

- For the assembly, we recommend the lead (Pb)-free thermal profile as per J-STD-020C.
  Power dissipation is 3.2 W at an ambient temperature of 25 °C when mounted on a double sided copper board using FR4 HTG, 70 µm of copper, 39 mm x 30 mm x 1.6 mm, with thermal vias.
- For other information about dissipation, see the Application Note 52027: "Thermal Management on SMD Thick Film Resistors (D2TO20, D2TO35, DTO25)".

STANDARD ELECTRICAL SPECIFICATIONS							
MODEL	SIZE	RESISTANCE RANGE $\Omega$	RATED POWER  P <sub>25 °C</sub> W	LIMITING ELEMENT VOLTAGE U <sub>L</sub> V	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C	CRITICAL RESISTANCE Ω
DTO25	TO-252 (DPAK)	0.016 to 700K	25	200	1, 2, 5, 10	150	1.6K

MECHANICAL SPECIFICATIONS			
Mechanical Protection Molded			
Resistive Element	Thick film		
Substrate	Alumina		
Connections	Tinned copper, Ni under layer		
Weight	2 g max.		

ENVIRONMENTAL SPECIFICATIONS				
Temperature Range	-55 °C to +150 °C			
Climatic Category	55 / 150 / 56			
	IEC 60695-11-5			
Flammability	2 applications 30 s separated by 60 s			

ELECTRICAL SPECIFICATIONS				
Tolerances	From 0.016 $\Omega$ to 0.047 $\Omega$ : ± 5 % and ± 10 % > 0.047 $\Omega$ to 0.1 $\Omega$ : ± 2 % to ± 10 % ≥ 0.11 $\Omega$ : ± 1 % to ± 10 %			
Power Rating and Thermal Resistance	25 W at +25 °C case temperature R <sub>TH (j - c)</sub> : 5 °C/W			
Temperature Coefficient	See Special Feature table ± 150 ppm/°C			
Dielectric Strength	1500 V <sub>RMS</sub> - 1 min - 15 mA max (between terminals and board)			
Insulation Resistance	$\geq 10^4 \ M\Omega$			
Inductance	≤ 0.1 µH			

DIMENSIONS			
Standard Package	TO-252 style (DPAK)		



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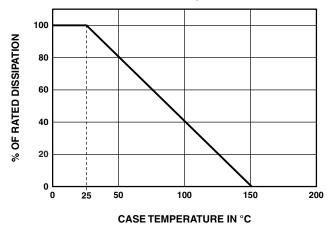
SPECIAL FEATURES					
Resistance Values	≥ 0.016	≥ 0.1	≥ 0.5		
Requirement Temperature Coefficient (TCR) (-55 °C +150 °C) IEC 60115-1	± 900 ppm/°C	± 350 ppm/°C	± 150 ppm/°C		

PERFORMANCE				
TESTS	CONDITIONS	REQUIREMENTS		
Momentary Overload	IEC 60115-1 §4.13 1.6 Pr 5 s US < 1.5 UL	± (0.25 % + 0.005 Ω)		
Load Life	IEC 60115-1 1000 h, 90/30 Pr at +25 °C	± (1 % + 0.005 Ω)		
High Temperature Exposure	AEC-Q200 REV D conditions: MIL-STD-202 method 108 1000 h, +175 °C, unpowered	± (1 % + 0.005 Ω)		
Temperature Cycling	AEC-Q200 REV D conditions: pre-conditioning 3 reflows according JESTD020D JESD22 method JA-104 1000 cycles, (-55 °C to +125 °C) dwell time 15 min	± (0.5 % + 0.005 Ω)		
Biased Humidity	AEC-Q200 REV D conditions: MIL-STD-202 method 103 1000 h, 85°C, 85 % RH	± (0.5 % + 0.005 Ω)		
Operational Life	AEC-Q200 REV D conditions: pre-conditioning 3 reflows according JESTD020D MIL-STD-202 method 108 1000 h, 90/30, powered, +125 °C	± (1 % + 0.005 Ω)		
ESD Human Body Model	AEC-Q200 REV D conditions: AEC-Q200-002 25 kV <sub>AD</sub>	± (0.5 % + 0.005 Ω)		
Vibration	AEC-Q200 REV D conditions: MIL-STD-202 method 204 20 g's for 20 min, 12 cycles test from 10 Hz to 2000 Hz	± (0.5 % + 0.005 Ω)		
Mechanical Shock	AEC-Q200 REV D conditions: MIL-STD-202 method 213 100 g's, 6 ms, 3.75 m/s 3 shocks/direction	± (0.5 % + 0.005 Ω)		
Board Flex	AEC-Q200 REV D conditions: AEC-Q200-005 bending 2 mm, 60 s	± (0.25 % + 0.01 Ω)		
Terminal Strength	AEC-Q200 REV D conditions: AEC-Q200-006 1.8 kgf, 60 s	± (0.25 % + 0.01 Ω)		

ASSEMBLY SPECIFICATIONS					
For the assembly on board, we recomm	For the assembly on board, we recommend the lead (Pb)-free thermal profile as per J-STD-020C				
TESTS	CONDITIONS	REQUIREMENTS			
Resistance to Soldering Heat	AEC-Q200 REV D MIL-STD-202 method 210 Solder Bath method: 270 °C / 10 s	± (0.5 % + 0.005 Ω)			
Moisture Sensitivity Level (MSL)	IPC / JEDEC <sup>®</sup> J-STD-020C 85 °C / 85 % RH / 168 h	Level: 1 + pass requirements of TCR Overload and Dielectic Strength after MSL			

## **POWER RATING**

The temperature of the case should be maintained within the limits specified.



### **CHOICE OF THE BOARD**

The user must choose the board according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 150 °C. The dissipated power is simply calculated by the following ratio:

$$P \, = \, \frac{\Delta T}{R_{TH \, (j \, - \, c)} + R_{TH \, (c \, - \, h)} + R_{TH \, (h \, - \, a)}} (1)$$

P: Expressed in W

ΔΤ: Difference between maximum working temperature and room temperature

Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance R<sub>TH (j - c)</sub>: of the component: 5 °C/W.

Thermal resistance value measured between outer side of the resistor and upper side of the board. This is the thermal resistance of the solder layer.

Thermal resistance of the board.  $R_{TH (h - a)}$ :

#### **Example:**

 $R_{TH~(c~-h)}$  +  $R_{TH~(h~-a)}$  for DTO25 power rating 3 W at ambient temperature +25 °C.

Thermal resistance R<sub>TH (j - c)</sub>: 5 °C/W

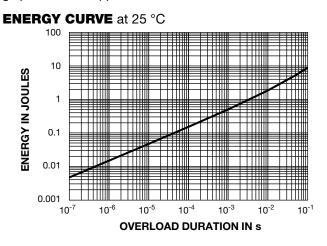
Considering equation (1) we have:

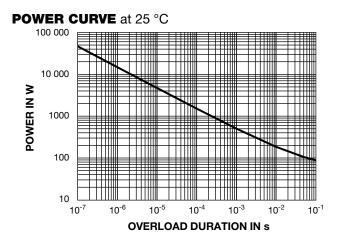
 $\Delta T = 150 \, ^{\circ}\text{C} - 25 \, ^{\circ}\text{C} = 125 \, ^{\circ}\text{C}$ 

 $\begin{array}{l} R_{TH~(j~-c)} + R_{TH~(c~-h)} + R_{TH~(h~-a)} = \Delta T/P = 125/3 = 41.7~^{\circ}C/W \\ R_{TH~(c~-h)} + R_{TH~(h~-a)} = 41.7~^{\circ}C/W - 5~^{\circ}C/W = 36.7~^{\circ}C/W \end{array}$ 

### **ACCIDENTAL OVERLOAD**

In any case the applied voltage must be lower than the maximum overload voltage of U<sub>s</sub> = 300 V. The values indicated on the graph below are applicable to resistors onto a board.





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## Single Pulse:

These informations are for a single pulse on a cold resistor at 25 °C (not already used for a dissipation) and for pulses of 100 ms maximum duration.

The formula used to calculate E is:

$$E = P \times t = \frac{U^2}{R} \times t$$

with:

E (J): Pulse energy P (W): Pulse power t (s): Pulse duration

U (V): Pulse voltage

 $R(\Omega)$ : Resistor

The energy calculated must be less than that allowed by the graph.

Repetitive or Superimposed Pulses:

The following formula is used to calculate the "equivalent" energy of a repetitive pulse or the "equivalent energy" of a pulse on a resistor that is already dissipating power.

$$E_{c} = E \times \left(1 + \frac{P_{a}}{P_{r}}\right)$$

with:

 $E_c$  (J): Equivalent pulse energy

E (J): Known pulse energy

P<sub>r</sub>: Resistor power rating

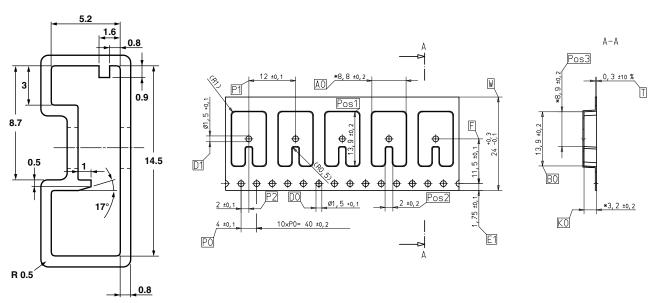
Pa: Mean power being dissipated

The energy calculated must be less than that allowed by the graph and the average power dissipated  $(P_a)$  must not exceed the continuous power of resistor.

## **PACKAGING**

• Tube: max. 50 units per tube

• Reel: max. 500 units per reel



### **MARKING**

Model, style, resistance value (in Ω), tolerance (in %), manufacturing date, Vishay Sfernice trademark.

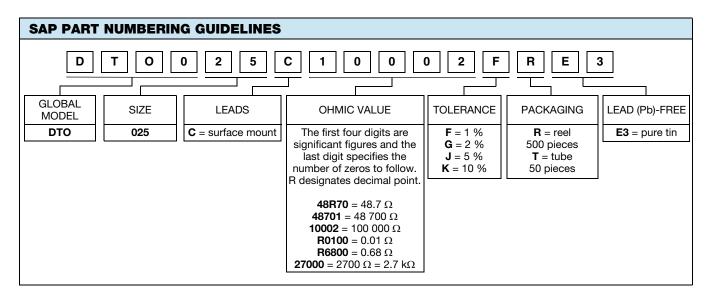




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ORDERIN	G INFORM	ATION				
DTO	025	С	100 kΩ	± 1 %	XXX	e3
MODEL	STYLE	CONNECTIONS	RESISTANCE VALUE	TOLERANCE	CUSTOM DESIGN	LEAD (Pb)-FREE
				$F = \pm 1 \%$ $G = \pm 2 \%$ $J = \pm 5 \%$ $K = \pm 10 \%$	Optional on request: shape, etc	





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