

I/O Cards

PEX-D24/PEX-D56

PCI Express, 24/56-ch OPTO-22 Compatible DIO Board



Features ▶▶▶▶

- PCI Express x1, Plug & Play
- DIO response time is about 2 us (500 kHz max.)
- Emulate two industrial-standard 8255 PPI ports (mode 0)
- D/O with higher driving capability
- Double side SMD, short card
- 24/56 buffered TTL digital I/O lines
- Three 8-bit bi-direction I/O ports
- 4 Interrupt sources
- Supports Card ID (SMD Switch)

Introduction

The PEX-D24/D56 is the new generation product that ICP DAS provides to meet RoHS compliance requirement, and is designed as easy replacement for the PIO-D24/PIO-D24U/PIO-D56/PIO-D56U. Users can replace the PIO-D24/PIO-D24U/PIO-D56/PIO-D56U by the PEX-D24/D56 directly without any software/driver modification.

The PEX-D24/D56 supports PCI Express bus and provides 24/56 TTL digital I/O lines. These lines are grouped into three 8-bit bi-direction ports that are named as port A (PA), port B (PB) and port C (PC). All ports are configured as inputs upon power-up or reset.

The PEX-D24/D56 adds a Card ID switch for users to recognize the board by the ID via software when using two or more PEX-D24/D56 cards in one computer.

Software

- DOS Lib and TC/BC/MSC sample program (with source codes)
- VB/VC/Delphi/BCB/VB.NET/C#.NET sample programs with source codes
- DLL and OCX SDK for 32-bit and 64-bit Windows XP/2003/Vista/2008/7
- Supports LabVIEW and Linux

Hardware Specifications

Models	PEX-D24	PEX-D56
Digital I/O		
I/O Channels	24-ch, 5 V TTL	56-ch, 5 V TTL
Input Logic Low	0.8 V max.	
Input Logic High	2.4 V min.	
Output Source Current	32 mA max.	
Output Sink Current	64 mA max.	
Programmable Interrupts	4	
General		
Bus Type	PCI Express x1	
Connectors	Female DB-37 x 1	Female DB-37 x 1, 20-pin Male box header x 2
Power Consumption	420 mA @ +5 V	580 mA @ +5 V
Operating Temperature	0 °C ~ +60 °C	
Storage Temperature	-20 °C ~ +70 °C	
Humidity	5 ~ 85% RH, non-condensing	

Ordering Information

PEX-D24 CR	PCI Express, 24-ch TTL DIO Board (RoHS)
PEX-D56 CR	PCI Express, 56-ch TTL DIO Board (RoHS)

Pin Assignments

