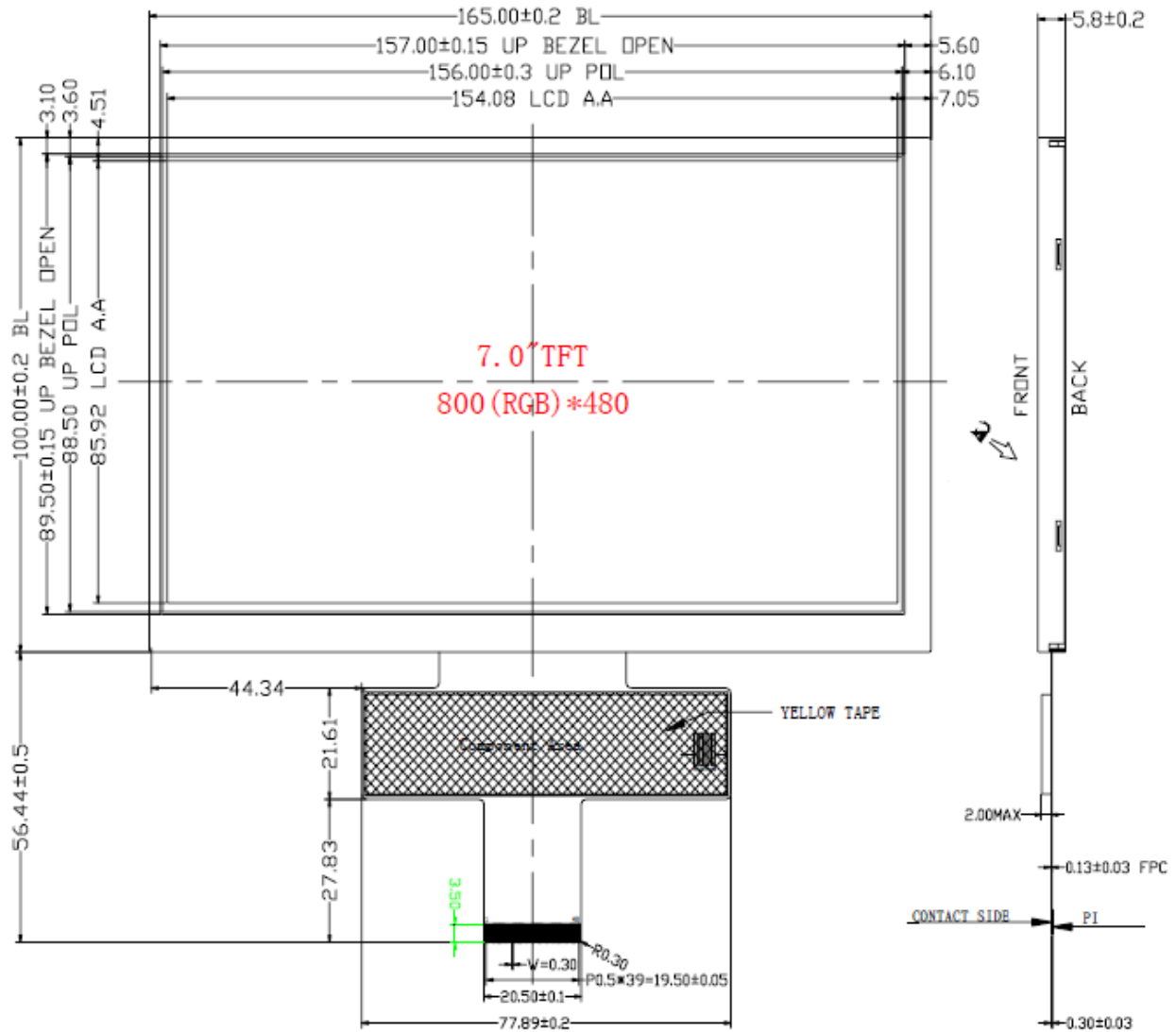


SPECIFICATION

7.0" TFT display **No Touch Panel**

- Active Matrix TFT LCD Module
- 800 x RGB x 480 Dots
- 65K/262K/16.7M Colors
- Interfaces: 16/18/24 bit RGB
- Connection Type: FPC Socket

Part Number: FDS800x480(165x100)RGE-70N



Features:

1. 7.0" TFT-LCD
2. 65K/262K/16.7M COLORS
3. DRIVER IC: EK9713/EK7330
4. OP: -20°C~70°C / ST: -30°C~80°C
5. ROHS Compliant

GENERAL DESCRIPTION

- 7.0" (diagonal), 800xRGBx480
- Transmissive/Normally White TFT module
- Viewing Direction: 12 o'clock
- Driving IC: EK9713/EK7330 or equivalent
- 450 NITS
- 16/18/24 bit RGB interface

General Specifications

PARAMETER	SPECIFICATIONS	UNIT
Outline dimensions	165.0(W) x 100.0(H) x 5.8(D) (Exclude FPC, cables of backlight)	mm
View area	156.0(W) x 88.5(H)	mm
LCD active area	154.08(W) x 85.92(H)	mm
Driver element	TFT Active Matrix	-
Pixel arrangement	RGB Vertical Stripe	-
Pixel pitch	0.0642(W) x 0.1790(H)	mm

ELECTRICAL Maximum Rating

ITEM	SYMBOL	MIN	MAX	UNIT	NOTE
Digital Supply voltage	V_{DD}	-0.5	5.0	V	
Digital Interface Supply voltage	V_{DDIO}	-0.5	$V_{DD}+0.3$	V	

ELECTRICAL Specifications

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
Digital supply voltage	V_{DD}	3.0	3.3	3.6	V	
Digital interface supply voltage	V_{DDIO}	3.0	3.3	3.6	V	
Normal mode current consumption	I_{DD}	--	150	--	mA	
Level input voltage	V_{IH}	$0.7V_{DDIO}$		V_{DDIO}	V	
	V_{IL}	GND		$0.3V_{DDIO}$	V	
Level output voltage	V_{OH}	$0.8V_{DDIO}$		V_{DDIO}	V	
	V_{OL}	GND		$0.2V_{DDIO}$	V	

Interface Pin Connections

PIN NO.	SYMBOL	DESCRIPTION	I/O
1	VLED-	Cathode pin of backlight	P
2	VLED+	Anode pin of backlight	P
3	GND	Ground	P
4	VDD	Supply voltage (3.3V)	P
5	R0	Red data input	I/O
6	R1	Red data input	I/O
7	R2	Red data input	I/O
8	R3	Red data input	I/O
9	R4	Red data input	I/O
10	R5	Red data input	I/O
11	R6	Red data input	I/O
12	R7	Red data input	I/O
13	G0	Green data input	I/O
14	G1	Green data input	I/O
15	G2	Green data input	I/O
16	G3	Green data input	I/O
17	G4	Green data input	I/O
18	G5	Green data input	I/O
19	G6	Green data input	I/O
20	G7	Green data input	I/O
21	B0	Blue data input	I/O
22	B1	Blue data input	I/O
23	B2	Blue data input	I/O
24	B3	Blue data input	I/O
25	B4	Blue data input	I/O
26	B5	Blue data input	I/O
27	B6	Blue data input	I/O
28	B7	Blue data input	I/O

Interface Pin Connections (continued)

PIN NO.	SYMBOL	DESCRIPTION	I/O
29	GND	Ground	P
30	PCLK	Dot clock signal for RGB interface operation. Fix this pin at VCI or GND when not in use.	I
31	DISP	Standby setting for testing, it should be connected to VDDIO in normal operation mode. If connected to GND, the IC is in standby mode.	I
32	HSYNC	Line synchronizing signal for RGB interface operation. Fix this pin at VCI or GND when not in use.	I
33	VSYNC	Frame synchronizing signal for RGB interface operation. Fix this pin at VCI or GND when not in use.	I
34	DE	Data enable signal for RGB interface operation. Fix this pin at VCI or GND when not in use.	I
35	NC	NC	
36	GND	Ground	P
37	XR(NC)	Touch panel Right Glass Terminal	A/D
38	YD(NC)	Touch panel Bottom Film Terminal	A/D
39	XL(NC)	Touch panel Left Glass Terminal	A/D
40	YU(NC)	Touch panel Top Film Terminal	A/D

Environmental Conditions

ITEM	Operating Temperature (Topr)		Storage Temperature (Tstg)		REMARK
	MIN	MAX	MIN	MAX	
Operating Temperature	-20°C	+70°C	-30°C	+80°C	Dry

Note 1: Product cannot sustain at extreme storage conditions for long time.