## Alphanumeric vacuum fluorescent displays

RS stock numbers 206-9001, 206-9017, 206-9023, 206-9039,
206-9045, 206-9067, 206-9073

## Introduction

Intelligent, alphanumeric, dot matrix vacuum fluorescent displays with integral display controller, drives and dc/dc converter. The modules utilise an emissive technology to provide a wide viewing angle and bright image. Light is emitted from a wide band blue-green phosphor having a peak wavelength of 550mm. The modules utilise a $5 \times 7$ dot matrix format and operate from a 5 V power supply.
The display controller has a character generator with up to 220 characters. Data communication with a host system is via a serial or parallel interface.
Figures 1 and 2 show the typical construction of a vacuum fluorescent display (VFD), an aluminium wiring pattern, a carbon insulation layer and an anode pattern are printed on a glass base plate. a grid mesh, etched from nickel alloy, is positioned above the anodes. Grid and anodes are connected to the terminal pins via the wiring pattern on the base plate. Filament wires (cathodes) are stretched above the grid and anodes and anchored at each end of the base plate. The complete structure is then sealed in a glass package at $500^{\circ} \mathrm{C}$ and evacuated to $10^{-7}$ torr, to ensure free electron movement.

## Applications

- Word processing equipment
- Electronic typewriters
- Mechanical instruments
- Point of sale terminals
- Test instruments
- Data terminals.


## Features

- Either parallel or serial input can be selected
- 5V power supply
- Brightness controllable levels by using dimming function
- Bright blue-green display
- Modules can be connected to system due directly
- Compact construction.


Figure 1


Figure 2


Absolute maximum ratings

| Parameter | Symbol | Min. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Supply <br> voltage | $\mathrm{V}_{\mathrm{CC}}$ | - | 6.5 | V |
| Input signal <br> voltage | $\mathrm{V}_{\mathrm{IS}}$ | -0.3 | 5.5 | V |
| Operating <br> temperature | $\mathrm{T}_{\mathrm{op} *}$ | -20 | +70 | ${ }^{\circ} \mathrm{C}$ |
| Storage <br> temperature | $\mathrm{T}_{\text {stg }}$ | -20 | +70 | ${ }^{\circ} \mathrm{C}$ |
| Relative <br> humidity <br> (Operating) | $\mathrm{H}_{\mathrm{Op}}$ | 20 | 85 | $\%$ |
| Relative <br> humidity <br> (Storage) | $\mathrm{H}_{\mathrm{Stg}}$ | 20 | 90 | $\%$ |

Electrical characteristics ( $20 \times 1$, 5 mm high characters,, $40 \times 2$, 5 mm high characters)

| Parameter | Symbol | $\begin{gathered} \text { Test } \\ \text { condition } \end{gathered}$ | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | - | 4.5 | 5.0 | 5.5 | V |
| Supply current (20×1) | $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | 150 | 250 | mA |
| Supply current $(40 \times 2)$ | $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | 750 | 1000 | mA |
| High level input voltage | $\mathrm{V}_{\text {IH }}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 2.0 | - | - | V |
| High level input current | $\mathrm{I}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}$ | - | - | 20 | $\mu \mathrm{A}$ |
| Low level input voltage | V IL | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | - | 0.8 | V |
| Low level input current | ILL | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}$ | - | - | -0.36 | mA |
| High level output voltage | VOH | $\begin{gathered} \mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V} \\ \mathrm{I}_{\mathrm{OH}}=2.6 \mathrm{~mA} \end{gathered}$ | 2.4 | - | - | V |
| Low level output voltage | $\mathrm{V}_{\text {OL }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{OL}}=12 \mathrm{~mA} \\ & \hline \end{aligned}$ | 0.25 | - | 0.4 | V |
| Luminance | L | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 340 | 690 | - | $\mathrm{cd} / \mathrm{m}^{2}$ |

$20 \times 2,4.16 \mathrm{~mm}$ high characters, $20 \times 2,4.65 \mathrm{~mm}$ high characters, $20 \times 2,5.5 \mathrm{~mm}$ high characters

| Parameter | Symbol | Test condition | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | - | 4.5 | 5.0 | 5.5 | V |
| Supply current <br> (4.16 character) | $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | 250 | 350 | mA |
| Supply current <br> (4.65 character) | $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}=5$ | - | 350 | 400 | mA |
| Supply current <br> (5.5 character) | $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}=5$ | - | 1000 | 1500 | mA |
| High level input <br> voltage | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{CC}}=5$ | 3.5 | - | - | V |
| High level input <br> current | $\mathrm{I}_{\mathrm{HH}}$ | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}$ | - | - | 1 | $\mu \mathrm{~F}$ |
| Low level input <br> voltage | $\mathrm{V}_{\mathrm{IL}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | - | 1.5 | V |
| Low level input <br> current | $\mathrm{I}_{\mathrm{L}}$ | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}$ | -0.22 | -0.11 | 0.05 | mA |
| High level output <br> voltage | $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}$ <br> $\mathrm{I}_{\mathrm{OH}}=0.5 \mathrm{~mA}$ | 3.6 | - | - | V |
| Low level output <br> voltage | $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}$ <br> $\mathrm{I}_{\mathrm{OL}}=0.5 \mathrm{~mA}$ | - | - | 0.9 | V |
| Luminance <br> $(4.16$ character) | L | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 340 | 690 | - | $\mathrm{cd} / \mathrm{m}^{2}$ |
| Luminance <br> $(4.65 ~ \& ~ 5.5 ~ c h a r a c t e r) ~$ | L | $\mathrm{~V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 350 | 700 | - | $\mathrm{cd} / \mathrm{m}^{2}$ |

$20 \times 4,5 \mathrm{~mm}$ high characters

| Parameter | Symbol | Test <br> condition | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | - | 4.5 | 5.0 | 5.5 | V |
| Supply current | $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | 750 | 1000 | mA |
| High level <br> input voltage | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 2.0 | - | - | V |
| High level <br> input current | $\mathrm{I}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{IH}}=2.0 \mathrm{~V}$ | - | - | 20 | $\mu \mathrm{~A}$ |
| Low level <br> input voltage | $\mathrm{V}_{\mathrm{IL}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | - | 0.8 | V |
| Low level <br> input current | $\mathrm{I}_{\mathrm{LL}}$ | $\mathrm{V}_{\mathrm{IL}}=0.8 \mathrm{~V}$ | - | - | -0.4 | mA |
| High level <br> output voltage | $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ <br> $\mathrm{I}_{\mathrm{OH}}=1 \mathrm{~mA}$ | 4.0 | - | - | V |
| Low level <br> output voltage | $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ <br> $\mathrm{I}_{\mathrm{OL}}=4 \mathrm{~mA}$ | - | - | 0.45 | V |
| Luminance | L | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 340 | 690 | - | $\mathrm{cd} / \mathrm{m}^{2}$ |

$20 \times 4,11.3 \mathrm{~mm}$ high characters

| Parameter | Symbol | Test <br> condition | Min. | Typ. | Max. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{CC}}$ | - | 4.5 | 5.0 | 5.5 | V |
| Supply current | $\mathrm{I}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | 2200 | 2700 | mA |
| High level <br> input voltage | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 2.6 | - | - | V |
| High level <br> input current | $\mathrm{I}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | - | 20 | $\mu \mathrm{~A}$ |
| Low level <br> input voltage | $\mathrm{V}_{\mathrm{IL}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | - | - | 0.5 | V |
| Low level <br> input current | $\mathrm{I}_{\mathrm{IL}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | -0.6 | - | - | mA |
| High level <br> output voltage | $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ <br> $\mathrm{I}_{\mathrm{OH}}=2 \mathrm{~mA}$ | 3.5 | - | - | V |
| Low level <br> output voltage | $\mathrm{V}_{\mathrm{OL}}$ | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ <br> $\mathrm{I}_{\mathrm{OL}}=6 \mathrm{~mA}$ | - | - | 0.5 | V |
| Luminance | L | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | 300 | 600 | - | $\mathrm{cd} / \mathrm{m}^{2}$ |

Module block diagram, $20 \times 1,5 \mathrm{~mm}$ high characters


Module block diagram, $20 \times 2,4.16 \& 4.65 \mathrm{~mm}$ high characters


Module block diagram, $20 \times 2,10.5 \mathrm{~mm}$ high characters


Module block diagram, $20 \times 4,5 \mathrm{~mm}$ high characters


Module block diagram, $20 \times 4,11.3 \mathrm{~mm}$ high characters


Module block diagram, $40 \times 2$, 5 mm high characters


## Mechanical dimensions

$20 \times 1,5 \mathrm{~mm}$ high characters (RS stock no. 206-9001)


| Pin no. | Signal | Serial in | Parallel in | Pin no. | Signal | Serial in | Parallel in |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | D7 | NC | $\bigcirc$ | 2 | +5V | $\bigcirc$ | $\bigcirc$ |
| 3 | D6 | NC | $\bigcirc$ | 4 | $+5 \mathrm{~V}$ | $\bigcirc$ | $\bigcirc$ |
| 5 | D5 | NC | $\bigcirc$ | 6 | $+5 \mathrm{~V}$ | $\bigcirc$ | $\bigcirc$ |
| 7 | D4 | NC | $\bigcirc$ | 8 | GND | $\bigcirc$ | $\bigcirc$ |
| 9 | D3 | NC | $\bigcirc$ | 10 | GND | $\bigcirc$ | $\bigcirc$ |
| 11 | D2 | NC | $\bigcirc$ | 12 | GND | $\bigcirc$ | $\bigcirc$ |
| 13 | D1 | NC | $\bigcirc$ | 14 | GND | $\bigcirc$ | $\bigcirc$ |
| 15 | D0 | NC | $\bigcirc$ | 16 | $\overline{\text { TEST }}$ | $\bigcirc$ | $\bigcirc$ |
| 17 | $\overline{\mathrm{WR}}$ | NC | $\bigcirc$ | 18 | $\overline{\text { SEL }}$ | NC | $\bigcirc$ |
| 19 | RXD | $\bigcirc$ | NC | 20 | BUSY | $\bigcirc$ | $\bigcirc$ |

NC : NO-CONNECTION
O : CONNECTION
$20 \times 2,4.16 \mathrm{~mm}$ high characters (RS stock no. 206-9017)


| Pin no. | Signal | Serial in | Parallel in | Pin no. | Signal | Serial in | Parallel in |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | D7 | NC | $\bigcirc$ | 2 | 5 V | $\bigcirc$ | $\bigcirc$ |
| 3 | D6 | NC | $\bigcirc$ | 4 | 5V | $\bigcirc$ | $\bigcirc$ |
| 5 | D5 | NC | $\bigcirc$ | 6 | 5 V | $\bigcirc$ | $\bigcirc$ |
| 7 | D4 | NC | $\bigcirc$ | 8 | GND | $\bigcirc$ | $\bigcirc$ |
| 9 | D3 | NC | $\bigcirc$ | 10 | GND | $\bigcirc$ | $\bigcirc$ |
| 11 | D2 | NC | $\bigcirc$ | 12 | GND | $\bigcirc$ | $\bigcirc$ |
| 13 | D1 | NC | $\bigcirc$ | 14 | GND | $\bigcirc$ | $\bigcirc$ |
| 15 | D0 | NC | $\bigcirc$ | 16 | TEST | $\bigcirc$ | $\bigcirc$ |
| 17 | $\overline{\mathrm{W}} \mathrm{R}$ | NC | $\bigcirc$ | 18 | $\overline{\text { SEL }}$ | NC | $\bigcirc$ |
| 19 | RXD | $\bigcirc$ | NC | 20 | BUSY | $\bigcirc$ | $\bigcirc$ |

NC : NO-CONNECTION
O : CONNECTION
$20 \times 2,4.65 \mathrm{~mm}$ high characters (RS stock no. 206-9023)


| Pin no. | Signal | Serial in | Parallel <br> in | Pin no. | Signal | Serial in | Parallel <br> in |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | D 7 | NC | O | 2 | D 6 | NC | O |
| 3 | D 5 | NC | O | 4 | D 4 | NC | O |
| 5 | D 3 | NC | O | 6 | D 2 | NC | O |
| 7 | Dl | NC | O | 8 | D 0 | NC | O |
| 9 | $\overline{\mathrm{WR}}$ | NC | O | 10 | $\overline{\text { SEL }}$ | NC | O |
| 11 | RXD | O | NC | 12 | BUSY | O | O |
| 13 | GND | O | O | 14 | GND | O | O |
| 15 | VCC | O | O | 16 | $\overline{\mathrm{TEST}}$ | O | O |

NC : NO-CONNECTION O : CONNECTION
$20 \times 2,10.5 \mathrm{~mm}$ high characters (RS stock no. 206-9039)



NC : NO-CONNECTION
O : CONNECTION
$20 \times 4,5 \mathrm{~mm}$ high characters (RS stock no. 206-9045)


| Pin no. | Signal | Serial in | Parallel in | Pin no. | Signal | Serial in | Parallel in |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | D7 | NC | $\bigcirc$ | 2 | +5V | $\bigcirc$ | $\bigcirc$ |
| 3 | D6 | NC | $\bigcirc$ | 4 | $+5 \mathrm{~V}$ | $\bigcirc$ | $\bigcirc$ |
| 5 | D5 | NC | $\bigcirc$ | 6 | $+5 \mathrm{~V}$ | $\bigcirc$ | $\bigcirc$ |
| 7 | D4 | NC | $\bigcirc$ | 8 | GND | $\bigcirc$ | $\bigcirc$ |
| 9 | D3 | NC | $\bigcirc$ | 10 | GND | $\bigcirc$ | $\bigcirc$ |
| 11 | D2 | NC | $\bigcirc$ | 12 | GND | $\bigcirc$ | $\bigcirc$ |
| 13 | D1 | NC | $\bigcirc$ | 14 | GND | $\bigcirc$ | $\bigcirc$ |
| 15 | D0 | NC | $\bigcirc$ | 16 | TEST | $\bigcirc$ | $\bigcirc$ |
| 17 | $\overline{\mathrm{WR}}$ | NC | $\bigcirc$ | 18 | $\overline{\text { SEL }}$ | NC | $\bigcirc$ |
| 19 | RXD | $\bigcirc$ | NC | 20 | BUSY | $\bigcirc$ | $\bigcirc$ |

NC : NO-CONNECTION
O : CONNECTION
$20 \times 4,11.3 \mathrm{~mm}$ high characters (RS stock no. 206-9067)


| Pin no. | Signal | Pin no. | Signal |
| :---: | :---: | :---: | :---: |
| 1 | D 7 | 2 | GND |
| 3 | D 6 | 4 | GND |
| 5 | D 5 | 6 | GND |
| 7 | D 4 | 8 | GND |
| 9 | D 3 | 10 | GND |
| 11 | D 2 | 12 | GND |
| 13 | Dl | 14 | GND |
| 15 | D 0 | 16 | GND |
| 17 | $\overline{\mathrm{WR}}$ | 18 | GND |
| 19 | $\mathrm{A0}$ | 20 | GND |
| 21 | $\overline{\mathrm{RD}}$ | 22 | GND |
| 23 | $\overline{\mathrm{SS}}$ | 24 | GND |
| 25 | $\overline{\mathrm{TEST}}$ | 26 | GND |
| 27 | BUSY | 28 | GND |
| 29 | $\overline{\mathrm{BL}}$ | 30 | GND |
| 31 | $\overline{\mathrm{RESET}}$ | 32 | GND |
| 33 | NC | 34 | GND |

$40 \times 2,5 \mathrm{~mm}$ high characters (RS stock no. 206-9073)


| Pin no. | Signal | Serial in | Parallel <br> in | Pin no. | Signal | Serial in | Parallel <br> in |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | D7 | NC | O | 2 | +5 V | O | O |
| 3 | D 6 | NC | O | 4 | +5 V | O | O |
| 5 | D 5 | NC | O | 6 | +5 V | O | O |
| 7 | D 4 | NC | O | 8 | GND | O | O |
| 9 | D 3 | NC | O | 10 | GND | O | O |
| 11 | D 2 | NC | O | 12 | GND | O | O |
| 13 | D 1 | NC | O | 14 | GND | O | O |
| 15 | D 0 | NC | O | 16 | $\overline{T E S T}$ | O | O |
| 17 | $\overline{\mathrm{WR}}$ | NC | O | 18 | $\overline{\mathrm{SEL}}$ | NC | O |
| 19 | RXD | O | NC | 20 | BUSY | O | O |

NC : NO-CONNECTION
O : CONNECTION

## Functions

The modules have data and control code write, self test and power on reset functions.

## Data and control code write-in

$20 \times 1$, 5mm character height (RS stock no. 206-9001)
$40 \times 2$, 5mm character height (RS stock no. 206-9073)

| $\overline{\text { TEST }}$ | $\overline{\mathrm{WR}}$ | $\overline{\mathrm{SEL}}$ | FUNCTION |
| :---: | :---: | :---: | :---: |
| l or open | $0 \rightarrow 1$ | 0 | Data and control code <br> write |
| 0 | - | - | Self test mode |

$20 \times 2,4.16 \mathrm{~mm}$ character height (RS stock no. 206-9017)

|  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
|  | TEST | $\overline{\mathrm{SEL}}$ | $\overline{\mathrm{WR}}$ | $\overline{\mathrm{RXD}}$ | Function |
| Parallel and <br> Serial interface | L | H or L | H or L | H or L | Self test |
| Parallel interface | H or NC | L | $\uparrow$ | NC | Data and control <br> code write in |
| Serial interface | H or NC | NC | NC | $*$ | Data and control <br> code write in |

L : Low level (OV)
H : High level (5V)
NC : non connection
$\uparrow$ : low to high transition

* : RXD (Serial input)
$20 \times 2,4.65 \mathrm{~mm}$ character height (RS stock no. 206-9023) $20 \times 2,10.5 \mathrm{~mm}$ character height (RS stock no. 206-9039)

|  | $\overline{T E S T}$ | $\overline{\mathrm{SEL}}$ | $\overline{\mathrm{WR}}$ | $\overline{\mathrm{RXD}}$ | Function |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Parallel and <br> Serial interface | L | H or L | H or L | H or L | Self test |
| Parallel interface | H or NC | L | $\uparrow$ | NC | Data and control <br> code write in |
| Serial interface | H or NC | NC | NC | $*$ | Data and control <br> code write in |

$20 \times 4$, 5mm character height (RS stock no. 206-9045)

|  | $\overline{T E S T}$ | $\overline{\mathrm{SEL}}$ | $\overline{\mathrm{WR}}$ | Function |
| :--- | :---: | :---: | :---: | :---: |
| Parallel and <br> Serial interface | 0 | - | - | Self test mode |
| Parallel interface | 1 | 0 | 0 | Data and control <br> code write |
| Serial interface | 1 | NC | NC | Data and control <br> code write |

## NC : No connection

0 : Low level (OV)
1 : High level (5V)
$20 \times 4,11.3 \mathrm{~mm}$ character height (RS stock no. 2069067)

| $\overline{\mathrm{SS}}$ | $\overline{W R}$ | $\overline{\mathrm{RD}}$ | $\mathrm{A}_{0}$ | $\overline{\mathrm{BL}}$ | $\overline{\text { RESET }}$ | $\overline{\text { TEST }}$ | BUSY | FUnCTIon | Data Bus Direction |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |  |  | HOST | MODULE |
| 0 | 0-1 | 1 | 0 | - | 1 | - | 0 | Data write-in |  | $\longrightarrow$ |
| 0 | 0-1 | 1 | 1 | - | 1 | - | 0 | Command write-in |  | $\rightarrow$ |
| 0 | 1 | 0 | - | - | 1 | - | 0 | Data read-out |  |  |
| - | - | - | - | 0 | 1 | - | 0 | Blanking |  |  |
| - | - | - | - | - | 0 | - | - | Reset |  |  |
| - | - | - | - | - | - | 0 | 0 | Test <br> mode |  |  |

When the data is being written in, the Busy signal is active which indicates that the module is processing the data.
(When the module is processing, the BUSY signal is high 'l'.)
The data or control command is to be written in at the rising edge of $\overline{\mathrm{VR}}(0 \rightarrow 1)$, when $\overline{\mathrm{SEL}}$ or $\mathrm{A} 0=$ low ' 0 '.
After a character is written in, the write-in position will be shifted to the right one digit automatically.
The above action can be executed only when the BUSY signal is low ' 0 '.

Character fonts
$20 \times 1,5 \mathrm{~mm}$ character height (RS stock no. 206-9001)
$40 \times 2,5 \mathrm{~mm}$ character height (RS stock no. 206-9073)

|  |  |  |  |  |  |  |  |  |  |  |  | 1 |  | $\begin{aligned} & 1 \\ & 0 \\ & 0 \\ & 1 \end{aligned}$ | $\begin{aligned} & 1 \\ & 0 \\ & 1 \\ & 0 \end{aligned}$ | $\begin{aligned} & 1 \\ & 0 \\ & 1 \\ & 1 \\ & \hline \end{aligned}$ |  | $\begin{array}{r} 1 \\ \hline \end{array}$ | 1 1 1 0 |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| D3 D2 D1 D0 |  | 0 | 1 | 2 | 3 | 3 | 4 | 5 | 6 |  | 7 | 8 | 9 | , | A | B | C | D | E | F |
| 0000 | 0 |  | DP | sp |  | 0 @ | @ | P | P |  | p | Ç | È | E | á | $\alpha$ | § | A | Й | Й |
| 00 | 1 |  |  | ! | 1 | 1 | A | Q | Q | a | q | ü | æ | æ | í | $\beta$ | IE | E | ת |  |
| 0010 | 2 |  |  |  | 2 | 2 | B | R | R |  | r | é | $\nrightarrow$ | モ | ó | $\gamma$ | IR | U |  | $\Pi$ \# |
| 0011 | 3 |  |  | \# | 3 | 3 | C | S | S |  | s | â | à ô | ô | ú | $\Delta$ |  | Ë | y |  |
| 0100 | 4 |  | DDC4 | C4 \$ | 4 | 4 | D | T | d | d | t | ä | à | ö | ñ | $\epsilon$ | $\bar{x}$ | İ | $\Phi$ | Ф |
| 0101 | 5 |  |  | \% |  |  | E |  |  |  | u | à | à ò | ò | N | $\eta$ | A | Â | Ц |  |
| 0110 | 6 |  |  | \& |  | 6 | F | V | $\checkmark$ |  | $v$ | à | U U | Û | a | $\theta$ |  | Ê |  |  |
| 01 | 7 |  |  |  |  | 7 |  | W | N | g | w | ç | u | u | - | $\lambda$ |  |  | Ш |  |
| 10 | 8 |  |  |  |  |  | H |  |  |  | x | ê | y | y | i | $\mu$ | $x$ | Ô | щ |  |
| 100 | 9 | нт |  | ) | 9 | 9 | I | Y | Y |  | y | ë | Ö | Ö |  | $\pi$ | $\checkmark$ | U | ъ |  |
| 1010 | A | LF |  |  |  |  | J | Z | Z |  | z | è | Ü | Ü | $\neg$ | $\rho$ | $\pm$ | b |  |  |
| 10 | B |  |  | + |  |  | K |  | [ k |  | \{ | i | $\phi$ | ¢ | $\frac{1}{2}$ | $\delta$ |  | $\Gamma$ | 「Э |  |
| 11 | C |  |  |  |  |  | L |  |  |  | i | $\hat{\imath}$ | i £ | £ | $\frac{1}{4}$ | ¢ |  |  |  |  |
| 1101 | D | CR |  |  |  | = | M |  | ] m |  | \} | i | i $\#$ | ¥ | i | ¢ |  | Ж | я |  |
| 1110 | E |  |  |  |  | $>\mathrm{N}$ | N |  | ^ n |  | ~ | Ä | Ä P | Pt | < | $\Omega$ |  | 3 |  |  |
| 1111 | F |  |  |  |  |  |  |  |  |  |  |  | A |  | $\geqslant$ | $\Sigma$ |  |  |  |  |

Note: DC4 applies to $40 \times 2$, 5mm character height (RS stock no. 206-9073) only
SP: SPACE
FD: FULL DOT

|  | $\begin{array}{\|l\|l} \mathrm{D7} \\ \mathrm{D6} \\ \mathrm{D5} \\ \mathrm{D} 4 \end{array}$ | 0 | 0 0 0 0 1 | 1 | 0 0 1 0 | 0 <br> 0 <br> 1 <br> 1 <br> 1 | 0 <br> 1 <br> 0 <br> 0 <br> 0 | 0 <br> 1 <br> 0 | 0 1 1 0 | 0 1 1 1 1 |  |  | $\begin{aligned} & 1 \\ & 0 \\ & 0 \\ & 0 \\ & 1 \end{aligned}$ | 1 0 1 0 | 1  <br> 0  <br> 1  <br> 1  <br> 1  | 1 1 <br> 1 1 <br> 1 1 <br> 0 0 <br> 0 1 | 1 1 1 0 | 1 1 1 1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| D3 D2 D1 D0 |  | 0 | 1 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 8 | 9 | A | B | C D | E | F |
| 0000 | 0 |  |  | DP | SP |  | \% |  | - | :- |  |  | $\because$ | : | $\cdots$ | : | T | $\because$ |
| 0001 | 1 |  |  |  | $!$ |  |  |  |  | $: \%$ |  |  | $\cdots$ |  | 7 | \%" |  | : |
| 0010 | 2 |  |  |  |  | $\cdots$ |  |  |  | '' |  |  | $\cdots$ | - | $\cdots$ |  | : | : |
| 0011 | 3 |  |  |  |  |  |  |  | .... | ": | : | 4 | $\therefore$ | $\ldots$ | " | \% |  | : |
| 0100 | 4 | गп |  |  |  | : |  |  | $\ldots$ | i |  |  | $\dot{!}$ | $\because$ | $\ldots$ | B: |  | $8$ |
| 0101 | 5 |  |  |  |  |  |  |  | $\cdots$ | $\vdots$ |  |  | $\cdots$ | " | $\because$ | In |  | $\cdots$ |
| 0110 | 6 |  |  |  |  |  |  |  | : | $\because$ |  |  | $\dot{7}$ | $\because$ | $1$ | $\ldots$ |  | $\div$ |
| 0111 | 7 |  |  | D |  |  |  |  | $\div$ | $1$ |  |  |  | $\because$ | $\dot{\mathrm{i}}$ | $\cdots$ |  | : |
| 1000 | 8 | BS |  |  |  |  |  |  |  | $\because$ |  |  | $\because$ | $\therefore$ | $\cdots$ | ? |  |  |
| 1001 | 9 | H |  |  | S | : |  |  |  | $\because$ |  |  | ㅍ. |  | $: \bar{u}$ | ¢ ${ }^{1}$ |  |  |
| 1010 | A |  |  |  | : | : |  |  | . | $\because$ |  |  | \#: |  |  | " |  | I.. |
| 1011 | B |  |  |  | $\cdots$ | : |  |  |  |  |  |  |  |  | $\overrightarrow{7}$ |  |  | $\because$ |
| 1100 | C |  |  |  | $:$ |  |  |  |  | $:$ |  |  |  |  | $\text { : } \because$ | ${ }^{\square}$ |  |  |
| 1101 | D |  |  |  |  |  |  |  | $07$ | ": |  |  |  |  |  |  |  | $\cdots$ |
| 1110 | E |  |  |  | : |  |  |  |  |  |  |  | $\ldots$ |  | $\because$ | $:$ | - | $\cdots$ |
| 1111 | F |  |  |  |  |  |  |  | $\ldots$ |  |  |  |  |  | : : : : | $\because$ | ! | SP |


|  | $\begin{array}{\|c} 07 \\ 06 \\ 05 \\ 05 \\ 04 \end{array}$ | 趧 | 0 | 0 |  | 0 1 0 0 | 0 1 1 0 1 | 0 <br> 1 <br> 1 <br> 0 |  | ( $\begin{aligned} & 1 \\ & 0 \\ & 0 \\ & 0 \\ & 0\end{aligned}$ | \|l|l|l|l | \|l $\begin{aligned} & 1 \\ & 0 \\ & 1 \\ & 0\end{aligned}$ | 1-1 $\begin{aligned} & 1 \\ & 0 \\ & 1 \\ & 1\end{aligned}$ | 1 | $\begin{array}{\|l\|} \hline 1 \\ 1 \\ 0 \end{array}$ |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| D3 D2 D1 Do |  | 0 | 1 | 2 | 3 | 4 | 5 | 5 | . 7 | 8 | - | A | B | C |  |  |  |
| 000 | 0 |  | DP | sp |  |  |  |  | $\cdots$ |  |  |  |  | \% |  |  |  |
| 0001 | 1 |  |  | 1 |  |  |  | = | . | .. | :- | . | 3 | \% | + |  |  |
| 010 | 2 |  |  |  |  |  |  | - | : | $\pm$ | 1 |  | , |  |  |  | $\pm$ |
| 0011 | 3 |  |  | $\not+$ |  |  |  | = | $\cdots$ | : |  |  |  |  |  |  |  |
| 0100 | 4 | गı |  | $\pm$ | : |  |  |  |  | . |  | : |  |  |  |  |  |
| 0101 | 5 |  |  |  |  |  |  |  |  | $\pm$ |  |  |  | -1 | \% |  |  |
| 0 | 6 |  |  |  |  |  |  | \% | - | $\pm$ |  | $=$ | $\cdots$ | $\cdots$ |  |  |  |
| 0111 | 7 |  | d |  |  | \% |  | = |  | $\bigcirc$ |  | : |  |  |  |  |  |
| 10 | 8 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| 1001 | 9 | нт |  |  |  |  |  | $\dot{1}$ | $\because$ |  |  |  |  |  |  |  |  |
| 1010 | A |  |  |  |  |  |  | I | ; | = |  |  |  |  | = |  |  |
| 10 | B |  |  |  |  | C | , | - |  | ! | + | 4 |  |  |  |  |  |
| 10 | C |  |  |  |  |  |  |  |  | " |  |  |  |  |  |  |  |
| 110 |  |  |  |  |  | $1$ |  | T | : | 3 |  |  |  |  |  |  |  |
| 1110 | E |  |  |  |  |  |  | + | : | $\cdots$ | F | * |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  | \% | 者 | \% | * | $\underset{\sim}{2}$ |  |  |  |  |

[^0]|  | $\begin{aligned} & \text { D7 } \\ & \text { D6 } \\ & \text { D5 } \\ & \text { D4 } \end{aligned}$ | $\begin{array}{\|l\|l} \hline 0 \\ 0 \\ 0 \\ 0 \\ \hline \end{array}$ | 0 |  | 0 0 1 0 | 0 <br> 0 <br> 1 <br> 1 <br> 1 | \|l|l| | 0 <br> 1 <br> 1 <br> 0 <br> 1 | 0 1 1 0 | 0 <br> 1 <br> 1 <br> 1 <br> 1 | $\begin{aligned} & 0 \\ & 1 \\ & 1 \\ & 1\end{aligned}$ | 1 <br> 0 <br> 0 <br> 0 | 1 0 0 1 1 | 1 <br> 0 <br> 1 <br> 0 | 1 <br> 0 <br> 1 <br> 1 <br> 1 | 1 <br> 1 <br> 0 <br> 0 | 1 <br> 1 <br> 0 <br> 1 <br> 1 | 1 1 1 0 | 1 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| D3 D2 D1 D0 | - | 0 | 1 |  | 2 | 3 | 4 | 5 | 6 | 7 | 7 | 8 | 9 | A | B | C | D | E | F |
| 0000 | 0 |  | D |  | Sp | \% | : | $\cdots$ |  | $\cdots$ | $\cdots:$ |  | $\cdots$ | - |  | \% | :-:" |  | $\pm$ |
| 0001 | 1 |  |  |  | $\vdots$ | . |  | . |  | $\ddot{\#}$ | $\cdots$ |  | $=$ | : | : | : |  |  | : |
| 0010 | 2 |  |  |  |  | : | $\cdots$ | $\cdots$ |  |  |  |  | $\therefore$ | $\cdots$ |  | : | $\because$ |  | : |
| 0011 | 3 |  |  |  |  | $\cdots$ |  |  |  |  |  | - | $\therefore$ | ... |  | - | $\cdots$ |  |  |
| 0100 | 4 | DIM |  |  |  | : |  |  |  | : |  |  | $\dot{\prime \prime}$ | $\because$ | $\cdots$ | $1$ | $\dot{P}$ |  |  |
| 0101 | 5 |  |  |  |  | :...: |  |  | $\cdots$ | 1 | .. |  | $\because$ | : | - | $\cdots$ |  |  | $\cdots$ |
| 0110 | 6 |  |  |  |  |  |  | \% | : |  |  |  | : | $\cdots$ | \% | ... | $\cdots$ |  | $\because$ |
| 0111 | 7 |  | D |  | " | 7 |  |  |  | : |  |  |  | $\because$ | $\because$ | $\cdots$ |  |  |  |
| 1000 | 8 | BS |  |  | $\square$ |  |  |  |  |  |  |  | $\because$ | $\because$ | \% | \% |  |  |  |
| 1001 | 9 | HT | то |  | : | : | T1 |  | $:$ |  |  |  | $\cdots$ |  |  |  |  |  |  |
| 1010 | A |  |  |  | \% | : | .. | - |  |  | ? |  | \% | $\cdots$ |  |  |  |  | !... |
| 1011 | B |  |  |  |  | $:$ |  | in |  |  |  |  |  | $7$ | $4$ |  | $0$ | $1$ | $\cdots$ |
| 1100 | C |  |  |  | : |  |  | : | ㄴ |  |  |  |  |  |  |  |  |  |  |
| 1101 | D | cL |  |  |  | = | B |  |  |  |  |  |  | $\ldots$ |  |  |  |  | . |
| 1110 | E |  |  |  | : |  |  |  |  |  |  |  |  | : | $\square$ | :: |  |  | $\cdots$ |
| 1111 | F |  |  |  |  |  |  | ..... | :-.. |  |  | $\because$ |  |  | : | $\because$ |  | ! | SP |

[^1]|  | $\left\lvert\, \begin{aligned} & 07 \\ & 06 \\ & 06 \\ & 05 \\ & 04 \end{aligned}\right.$ | \|ll | \|l |  |  |  | 0 <br> 0 <br> 1 <br> 1 <br> 0 <br> 1 | 0  <br> 0  <br> 1  <br> 1  <br> 1  <br> 0  |  |  | 1 <br> 0 <br> 0 <br> 0 <br> 1 | 1  <br> 0 1 <br> 0  <br> 1  <br> 1  <br> 0  | 1 <br> 0 <br> 1 <br> 1 | 1 <br> 1 <br> 0 <br> 0 | 1 <br> 1 <br> 0 <br> 1 |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| D3 D201 D0 |  | 0 | 1 | 2 |  | 34 | 5 | 5 | 67 | 8 | 8 | A | B | C | D |  | F | F |
| 000 | 0 |  | dP |  |  | 1 | \% | $\because$ | \% | \% | \% | \% | \% | : | + | \% | $\dot{\sim}$ |  |
| 0001 | 1 |  | DC1 | 1 |  | 1 | 1 | $1=$ | $\square$ | $\ddot{\square}$ | \% | $\because$ | 3 | W: | $\cdots$ |  |  | $\pm$ |
| 0010 | 2 |  |  |  |  | 3 | \% | \% | $\cdots$ | $\bigcirc$ | 6 | $\cdots$ | $\cdots$ | \% | C |  | $\div$ | $\because$ |
| 0011 | 3 | DEF |  |  | $\cdots$ | \% |  |  | $\cdots$ |  | : | $\%$ | 4 | 1 | - |  |  |  |
| 0100 | 4 | M |  |  |  | 4 |  | $\cdots$ | $1 \%$ | $\cdots$ | $\cdots$ | \% | $\cdots$ | $\because$ | T |  |  |  |
| 0101 | 5 |  |  |  |  | \% | C | $1 \times$ | $\pm$ | ) | $\cdots$ | 0 | 1 | - |  |  | $\because$ |  |
| 0110 | 6 |  |  | $\because$ |  | \% |  | \% | : |  | \% | : | $\cdots$ | $\because$ | . |  |  |  |
| 0111 | 7 |  |  |  |  | \% |  |  | 11 | $1 \times$ | \% | \% | $\because$ | $\because$ |  |  |  |  |
| 1000 | 8 | Bs |  |  |  | 1 | 1 | - | ; |  | \% | - | \# |  |  |  |  |  |
| 1001 | 9 | нт |  | 3 |  | 1 | \% | 1 | ' | \% | 0 | ; | T | 1 |  |  |  |  |
| 1010 | A | LF |  |  |  | T |  |  | $\cdots$ | . | 1 | 1 | $\cdots$ | 4 |  |  |  |  |
| 101 | B |  |  |  |  | - | . | : | $\because$ | . | 4 | : | \% |  |  |  |  |  |
| 1100 | C |  |  |  |  |  |  | 1 | . | " | $\pm$ | 4 | $\because$ |  |  |  |  |  |
| 01 | D |  |  |  |  | 1 | 1 | 1 | \% | \% | \% | 1 | ¢ | $: \text { UFo }$ |  |  |  |  |
| 1110 | E |  |  |  |  | 1 | ! |  | $\cdots$ | $\because$ | \% | $\dot{*}$ | 1 | UF1 |  |  |  |  |
| 1111 | F |  |  |  |  | T | 1 | ..." | 圜 | 3 | $\cdots$ | $\cdots$ | 2 | UF2 |  |  |  |  |



## Control code

The control codes are available as follows:

1. DIM: Dimming ( 04 HEX)
2. BS: Back Space (08 HEX)
3. HT: Horizontal Tab (09 HEX)
4. LF: Line Feed (0A HEX)
5. CR: Carriage Return (0D HEX)

5a. Clear (OD HEX) applies to $20 \times 2,4.16 \mathrm{~mm}$ character, $20 \times 2,4.65 \mathrm{~mm}$ character, $20 \times 2$, 10.5 mm character only
6. DP: Display Position (10 HEX)
7. DCl: Normal Display Mode (11 HEX)

7a. DCl: Auto Carriage Return Mode (11 HEX) $20 \times 1$ (RS stock no. 206-9001 only)
8. DC2: Vertical Scroll Mode (12 HEX)

8a. DC2: Over Write Mode (12 HEX) applies to $20 \times 1$ (RS stock no. 206-9001) only
9. DC3: Cursor On Mode (13 HEX)

9a. DC3: Horizontal Scroll Mode (13 HEX) applies to $20 \times 1$ (RS stock no. 206-9001 only)
10. DC4: Cursor Off Mode (14 HEX)
11. RST: Reset (1F HEX)
12. DC: Cursor Mode (17 HEX)
13. ALD: All Display (OF HEX)
14. TON: Triangle Mark On
15. TOF: Triangle Mark Off
16. Triangle Mark All Off
17. Define Characters UFO~2 (03 HEX)
18. VT: Vertical Tab (OBHEX)
19. ESC: Escape (lBHEX)

## 1. DIM (Dimming)

Brightness can be controlled into either four or five levels dependent upon module by using this function. After writing 04 H , another HEX byte is written to change the brightness output, see below.

$$
\begin{gathered}
\text { libyte } \\
\text { (DIM command code), } 04 \mathrm{H}
\end{gathered}+\quad \begin{aligned}
& \text { l byte } \\
& \text { Dimming level data }
\end{aligned}
$$

$20 \times 1$ (RS stock no. 206-9001)
$20 \times 4,5 \mathrm{~mm}$ character height (RS stock no. 206-9045)
$40 \times 2$ (RS stock no. 206-9073)

| Dimming level | Data |
| :---: | :---: |
| $100 \%$ | FFH |
| $60 \%$ | 60 H |
| $40 \%$ | 40 H |
| $20 \%$ | 20 H |

$20 \times 2,4.16 \mathrm{~mm}$ character height (RS stock no. 206-9017)
$20 \times 2,4.65 \mathrm{~mm}$ character height (RS stock no. 206-9023)
$20 \times 2,10.5 \mathrm{~mm}$ character height (RS stock no. 206-9039)

| Dimming level | Data |
| :---: | :---: |
| $100 \%$ | FFH |
| $80 \%$ | 80 H |
| $60 \%$ | 60 H |
| $40 \%$ | 40 H |
| $20 \%$ | 20 H |
| $0 \%$ | 00 H |

## 2. BS (Back Space)

DC1 MODE: The cursor position (write-in position) is shifted to the left one digit, (beyond this point, the position of the cursor is identical with write-in position of the display, unless otherwise specified. Under DC4 MODE, the cursor will not show up).
When the cursor is on the most significant digit of the second row, the cursor moves to the least significant digit of the first row.
When the cursor is on the most significant digit of the second row, the cursor moves to the least significant digit of the first row.
DC2 MODE: The same as above

## 4 row displays

The write-in position is shifted to the left one digit, (beyond this point, the position of write-in position is identical with write-in position of the display, unless otherwise specified. Under DC8 MODE, the write-in position will not show up).
When the write-in position is on the most significant digit of the third row, the write-in position moves to the least significant digit of the second row.
When the write-in position is on the most significant digit of the first row, the write-in position moves to the least significant digit of the fourth row.

## 3. HT (Horizontal tab):

DC1 MODE: The cursor position is shifted to the right one digit. When the cursor is on the least significant digit of the first row, the cursor moves to the most significant digit of the second row.
When the cursor is on the least significant digit of the second row, the cursor moves to the most significant digit of the first row.
DC2 MODE: When the cursor is on the least significant digit of the second row, the characters displayed in the second row are shifted up to the first row and the cursor moves to the most significant digit of the second row. Subsequently, the second row is cleared.

## 4 row displays

DC1 MODE: The write-in position is shifted to the right one digit. When the write-in position is on the least significant digit of the first row, the write-in position moves to the most significant digit of the second row.
When the write in position is on least significant digit of the fourth row, the write in position moves to the most significant digit of the first row.
DC2 MODE: The write in position is shifted to the right one digit. When the write-in position is on the least significant digit of the fourth row, the characters displayed in the fourth row are shifted up to the third row and the write-in position moves to the most significant digit of the fourth row. Subsequently, the fourth row is cleared.

## 4. LF (Line Feed):

DC1 MODE: The cursor moves up or down to another row staying on the same line.
DC2 MODE: When the cursor is in the second row, the character displayed there, is shifted up to the first row, leaving the cursor at its present position, then the second row is cleared.

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When the cursor is in the first row, the same as DCl MODE operation.

## 4 row displays

When the write-in position is in the fourth row, the character displayed second to fourth row, is shifted up, leaving the write-in position at its present position, then the fourth row is cleared.
When the write-in position is in first to third row, the write-in position moves down to the row below staying on the same line.

## 5. CR (Carriage Return):

DC1 MODE: The cursor moves to the most significant digit of the same row.
DC2 MODE: The same as DC1 MODE operation.

## 5a. CLR (Clear):

All the characters displayed are erased, the write-in position moves to the most significant digit of the first row. But the Dimming level and Cursor Mode are kept.

## 6. DP (Display Position):

Instead of writing the character from the first digit, the write-in starting position can be pointed by using this function.
After writing lOHEX to prepare the module for this command, another HEX byte is written to specify the position desired.
A third byte representing data is then sent.

| Format Char. $\times$ Line | Character Height | Row | The most significant digit | The least significant digit |
| :---: | :---: | :---: | :---: | :---: |
| $20 \times 1$ | 5 | - | OOHEX | 13HEX |
| $\begin{aligned} & 20 \times 2 \\ & 20 \times 2 \\ & 20 \times 2 \end{aligned}$ | $\begin{aligned} & 4.16 \\ & 4.65 \\ & 10.5 \end{aligned}$ | $\begin{aligned} & \text { 1st } \\ & \text { 2nd } \end{aligned}$ | $\begin{aligned} & \text { 00HEX } \\ & 14 \mathrm{HEX} \end{aligned}$ | $\begin{aligned} & 13 \mathrm{HEX} \\ & 27 \mathrm{HEX} \end{aligned}$ |
| $\begin{aligned} & 20 \times 4 \\ & 20 \times 4 \end{aligned}$ | $\begin{gathered} 5 \\ 11.3 \end{gathered}$ | 1st <br> 2nd <br> 3rd <br> 4th | $\begin{aligned} & \text { 00HEX } \\ & 14 \mathrm{HEX} \\ & 28 \mathrm{HEX} \\ & 3 \mathrm{CHEX} \end{aligned}$ | $\begin{aligned} & \text { 13HEX } \\ & \text { 27HEX } \\ & \text { 3BHEX } \\ & \text { 4FHEX } \end{aligned}$ |
| $40 \times 2$ | 5 | $\begin{gathered} \text { 1st } \\ \text { 2nd } \end{gathered}$ | $\begin{aligned} & \text { 00HEX } \\ & 28 \mathrm{HEX} \end{aligned}$ | $\begin{aligned} & 27 \mathrm{HEX} \\ & \text { 4FHEX } \end{aligned}$ |

## 7. DC 1 ( Normal Display Mode)

After writing a character, the cursor is shifted to the right one digit automatically.
When the cursor is on the least significant digit of the first row, the cursor moves to the most significant digit of the second row. When the cursor is on the least significant digit of the second row, the cursor moves to the most significant digit of the first row.

## 4 row displays

After writing a character, the write-in position is shifted to the right one digit automatically.
When the write-in position is on the least significant digit of the first to third row, the write-in position moves to the most significant digit of the row below.

When the character is displayed on the least significant digit of the fourth row, the write-in position is on the same digit.
And the character code is written in the module next, first, all digits are cleared, second, the character is displayed on the most significant digit of the first row and the write-in position moves to the next digit.
When the power is turned on, this DCl MODE is selected, and will be held until another mode is selected.

## 7a. DC1 (Auto Carriage Return)

All the characters displayed are erased, then write-in position is set on the most significant digit.
When a character is written to the least significant digit, the write-in position stays on this digit.
When the next data is written-in, all the characters displayed are erased, and this character is written to the most significant digit.
Then the write-in position is set on the second digit.

## 8. DC2 (Vertical Scroll Mode)

After writing the characters up to the least significant digit of the second row, all the characters displayed in the second row are shifted to the upper row (first row), clearing the second row:
When the power is turned on, this DC2 MODE is selected, and will be held until another mode is selected.

## 4 row displays

After writing the characters up to the least significant digit of the fourth row, all the characters displayed in the second to fourth row are shifted to the upper row, clearing the fourth row.

## 8a. DC2 (Over Write)

The write position is fixed on the least significant digit.

## 9. DC3 (Cursor On Mode)

The cursor is displayed. When the power is turned on, this DC3 MODE is selected and will be held until another mode ( DC 4 ) is selected.

## 9a. DC 3 (Horizontal Scroll)

All the characters displayed are shifted to the left one digit, and a character is written on the least significant digit.
The write-in position is fixed on this digit.

## 10. DC4 (Cursor OFF Mode)

The cursor will not be displayed.

## 11. RST (Reset)

Resetting the module.
All the characters displayed are erased, then the writein position (cursor position) is set on the most significant digit of the first row.
The displaying status is the same as the Power on Reset.
The display mode is set for DC 2 . The cursor mode is set for DC3,

## 12. DC (Cursor Mode)

After writing 17 HEX, another HEX byte is written to change the cursor mode.

| l byte |
| :---: |
| $(\mathrm{DC} 5$ command Code $)$ |$\div \quad$| (Select Mode Data) |
| :---: |

TABLE 9

| Select Mode | Data |
| :--- | :---: |
| Lighting | FFH |
| Blinking | 88 H |
| No Lighting | 00 H |

The cursor is always displayed at the write-in position. The cursor is formed by the 5 dots located at the bottom of the $5 \times 7$ dot matrix character font.
The cursor will be displayed as an over writing mode and the behaviour of the cursor under the lighting mode and blinking mode are explained below.

## 1. Lighting Mode

When the non displayed position is assigned as a write-in position, the cursor will be displayed there.
If a position that already has a character located is assigned, the character will be eliminated and the cursor displayed.

## 2. Blinking Mode

The cursor will be repeated ON and OFF every 0.3 seconds when the non-displayed position is selected for the write-in position.
If a position that already has a character located is selected (as a write-in position), the character and the cursor will be displayed alternately.

## 3. No Lighting Mode

The no lighting mode means that the cursor will not be displayed.
When the power is turned on, no lighting mode will be selected automatically.
Therefore, if the cursor is required, DC command shall be sent to select the cursor lighting or blinking mode.

## 13. ALD (All Display)

The full dots in all digits are displayed.
The dimming level is set for $100 \%$.
To release this mode, the module is turned off or the RST command shall be written.

## 14. TON (Triangle Mark On)

This command is Triangle Mark control code.
After writing 18 HEX, the successive another HEX byte of data will be accepted as the turn on position.
The most significant digit The least significant digit 14 HEX 27 HEX

## 15. TOF (Triangle Mark Off)

This command is Triangle Mark control code.
After writing 19 HEX, the successive another HEX byte of data will be accepted as the turn off position.

The most significant digit The least significant digit 14 HEX 27 HEX

## 16. TFF (Triangle Mark All Off)

All the Triangle Mark displayed are erased.

## 17. DEF (Define UFO~2)

The DEF command defines user definable characters, UFO~2. (up to 3 fonts)
These fonts are stored in the module as follows:

| l byte <br> DEF command <br> code | l byte <br> Position code | 5 byte <br> the font data |
| :---: | :---: | :---: |
| $(03 \mathrm{H})$ | $(\mathrm{CDH}$ to CFH$)$ |  |

a) C haracter font

| $1-1$ | $2-1$ | $3-1$ | $4-1$ | $5-1$ |
| :--- | :--- | :--- | :--- | :--- |
| $1-2$ | $2-2$ | $3-2$ | $4-2$ | $5-2$ |
| $1-3$ | $2-3$ | $3-3$ | $4-3$ | $5-3$ |
| $1-4$ | $2-4$ | $3-4$ | $4-4$ | $5-4$ |
| $1-5$ | $2-5$ | $3-5$ | $4-5$ | $5-5$ |
| $1-6$ | $2-6$ | $3-6$ | $4-6$ | $5-6$ |
| $1-7$ | $2-7$ | $3-7$ | $4-7$ | $5-7$ |

b) Font data

|  |  | b it |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|  | 1st | 1-1 | 2-1 | 3-1 | 4-1 | 5-1 | 1-2 | 2-2 | 3-2 |
|  | 2nd | 4-2 | 5-2 | 1-3 | 2-3 | 3-3 | 4-3 | 5-3 | 1-4 |
| Byte | 3rd | 2-4 | 3-4 | 4-4 | 5-4 | 1-5 | 2-5 | 3-5 | 4-5 |
|  | 4th | 5-5 | 1-6 | 2-6 | 3-6 | 4-6 | 5-6 | 1-7 | 2-7 |
|  | 5th | 3-7 | 4-7 | 5-7 | "0" | "0" | "0" | "0" | "0" |

Example of write-in character " 1 " in UFO.
Control and data strings $03 \mathrm{H}, \mathrm{FCH}, 23 \mathrm{H}, 08 \mathrm{H}, 42 \mathrm{H}$, $15 \mathrm{H}, \mathrm{COH}$.
a) Character

b) Font data

|  |  | b it |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
|  | 1st | 0 | 0 | 1 | 0 | 0 | 0 | 1 | 1 |
|  | 2nd | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
| Byte | 3rd | 0 | 1 | 0 | 0 | 0 | 0 | 1 | 0 |
|  | 4th | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 1 |
|  | 5th | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |

" 1 ": Turn On
" 0 ": Turn Off

## Defining User's Font

It is recommended to store these definable characters at the initialisation of the module. All the data will remain in the RAM. There is no back-up system of this RAM, therefore, restore the data when the power is off.

## 18. VT: Vertical Tab

DC1 MODE: The write in position is shifted up one row. When the write in position is in the first row, it moves to the fourth row.
DC2 MODE: The same as DCl mode operation.

## 19. ESC: Escape

The write in starting position can be pointed by using this function.

$$
1 \text { BHEX } \quad+\text { lByte } \quad+\text { lByte }
$$

(control code) (Vertical position) (Horizontal position)
First byte (Vertical)
0000 0000: 00HEX (lst row)
00000001 : 0lHEX (2nd row)
00000010 : 02HEX (3rd row)
00000011 : 03HEX (4th row)
next byte (Horizontal)
00000000 : 00HEX (lst digit)
00010011 : 13HEX (20th digit)

## Self-Test

$\overline{T e s t}=$ Low "0" (connector pin \#16 is connected to GND for all modules except $20 \times 4,11.3 \mathrm{~mm}$ character height, connector pin \#25 is connected to ground) starts the Self-Test. Then the display shows all characters, alphabet, numerics and symbols, in that order.
Eighty $(2 \times 40)$ characters are displayed at a time.
Using this mode, neither data write-in nor control code write-in is allowed.


## Power on Reset

When the module is turned on, the display and the memory are cleared and the module is initialised.
The display mode is set for DC 2 , and the cursor mode is set for DC3.
*For $20 \times 1$ module the display mode is set for set DCl,

## Selection of input mode

The pin connections on the mechanical dimensions drawings show the combinations of the signal lines for the parallel or serial input. User must choose one of the combinations.
Unused signal lines are to be open (internally pulled up).
The tables below shows the combinations of the signal lines for the parallel or serial input.

## Baud rate selection

$20 \times 1$, 5mm character height (RS stock no. 206-9001)
$40 \times 2$, 5 mm character height (RS stock no. 206-9073)
In case of serial input, it is possible to choose eight kinds of baud rate by Jl~J2 assham below, J3 clock select open: Outside Clock ( 307.2 KHz ) OSCl
short: Internal Clock (4.0 MHz) Xl

| J1 | short | open | short | open |
| :---: | :---: | :---: | :---: | :---: |
| J2 | short | short | open | open |
| J3 short | $62500(b p s)$ | 31250 | 15625 | 7812.5 |
| J3 open | 9600 | 4800 | 2400 | 1200 |

Note: J1, J2 are shorted and J3 is open when a module is supplied (9600bps is selected when serial input is used).

## Baud rate selection

$20 \times 2,4.16 \mathrm{~mm}$ character height (RS stock no. 2069017)

In case of serial input, it is possible to choose four kinds of baud rate by Jl~J2, as shown, below.

| J1 | Open | Short | Open | Short |
| :---: | :---: | :---: | :---: | :---: |
| J2 | Open | Open | Short | Short |
| baud rate | 9600 | 4800 | 2400 | 1200 |

## Baud rate selection

$20 \times 2$, 4.65 mm character height (RS stock no. 2069023)
$20 \times 2$, 10.5 mm character height (RS stock no. 2069039)

In case of serial input, it is possible to choose eight kinds of baud rate by Jl~J3, as shown below.

| J1 |  | Open | Short | Open |
| :---: | :---: | :---: | :---: | :---: |
| Short |  |  |  |  |
| J2 |  | Open | Open | Short |
| Short |  |  |  |  |
| J3 | Short | 62500 | 31250 | 15625 |
|  | Open | 9600 | 4800 | 2400 |

## Baud rate selection

$20 \times 4$, 5mm character height (RS stock no.206-9045)
In case of serial input, it is possible to choose eight kinds of baud rate by $\mathrm{Jl} \sim \mathrm{J} 3$, as shown below.
When serial input is selected, J5 must be short.

| J1 | short | open | short | open |
| :---: | :---: | :---: | :---: | :---: |
| J2 | short | short | open | open |
| J3 short | 1200 (bps) | 2400 | 4800 | 9600 |
| J3 open | 7812.5 | 15625 | 31250 | 62500 |

Note: When the module is supplied J1, J2, J3 and J5 are open.
The $20 \times 4,11.3 \mathrm{~mm}$ character height (RS stock no. 206-9067) module has a parallel interface only.

## Write-in timing

Ensure the BUSY signal is ' $L$ ', when data is to be written in.
$20 \times 1$, 5mm character height (RS stock no. 206-9001) $40 \times 2$, 5mm character height (RS stock no. 206-9073)


|  |  | Min. | Max. | Note |
| :---: | :---: | :---: | :---: | :---: |
| (1) | tsu (DATA) | 50ns | - |  |
| (2) | th (DATA) | 50ns | - |  |
| (3) | tsu ( $\overline{\text { SEL) }}$ | 50ns | - |  |
| (4) | th ( $\overline{\mathrm{SEL}})$ | 50ns | - |  |
| (5) | tpw ( $\overline{\mathrm{WR}})$ | 50ns | - |  |
| (6) | twait (1) | Ons | - |  |
| (7) | twait (2) | 250ns | - | For Min. 250ns, $\overline{\mathrm{WR}}$ should not be active (positive H), after BUSY is '0' |
| (8) | tdelay | - | 50ns |  |


(1) $\mathrm{t}(\mathrm{DATA})=10^{6} / \mathrm{b}$ bud rate $(\mu \mathrm{s})$
(This depends on the selection of the baud rate).
(2) t (DATA) $/ 2(\mu \mathrm{~s})$
(BUSY becomes ' 1 ' at the centre of stop bit).
(3) t (WVAIT) : $0(\mu \mathrm{~s})$
(START should be active (' 0 '), after BUSY is ' 0 ').
$20 \times 2$, 4.16mm character height (RS stock no. 2069017)


|  |  | Min. | Max. | Note |
| :---: | :---: | :---: | :---: | :---: |
| (1) | tsu (DATA) | 50ns | - |  |
| (2) | th (DATA) | 100ns | - |  |
| (3) | tsu ( $\overline{\mathrm{SEL}})$ | 50ns | - |  |
| (4) | th ( $\overline{\text { SEL) }}$ | 50ns | - |  |
| (5) | tpw ( $\overline{\mathrm{WR})}$ | 50ns | - |  |
| (6) | twait (1) | Ons | - |  |
| (7) | twait (2) | $1 \mu \mathrm{~s}$ | - |  |
| (8) | tdelay | - | 150ns |  |
| (9) | twait (3) | - | $45 \mu \mathrm{~s}$ |  |


(1) t (DATA) $=10^{6} /$ baud rate $(\mu \mathrm{s})$
(This depends on the selection of the baud rate).
(2) t (DATA) $/ 2(\mu \mathrm{~s})$
(BUSY becomes 'H' at the centre of stop bit).
(3) t (WAIT) : 2~45 ( $\mu \mathrm{s}$ )
$20 \times 2,4.65 \mathrm{~mm}$ character height (RS stock no. 206-9023) $20 \times 2,10.5 \mathrm{~mm}$ character height (RS stock no. 206-9039)


|  |  | Min. | Max. | Note |
| :---: | :---: | :---: | :---: | :---: |
| (1) | tsu (DATA) | 50ns | - |  |
| (2) | th (DATA) | 100 ns | - |  |
| (3) | tsu ( $\overline{\text { SEL) }}$ | 50ns | - |  |
| (4) | th ( $\overline{\mathrm{SEL}})$ | 50ns | - |  |
| (5) | twait ( $\overline{\mathrm{WR} 1}$ ) | 50ns | - |  |
| (6) | tpw (WR) | 50ns | - |  |
| (7) | tdelay | - | 150ns |  |
| (8) | tpw (BUSY) | - | $45 \mu \mathrm{~s}$ |  |
| (9) | twait (VR2) | $1 \mu \mathrm{~S}$ | - |  |


(1) $\mathrm{t}(\mathrm{DATA})=10^{6} / \mathrm{b}$ buud rate $(\mu \mathrm{s})$
(This depends on the selection of the baud rate).
(2) t (DATA) $/ 2(\mu \mathrm{~s})$
(BUSY becomes ' H ' at the centre of stop bit).
(3) t (WVAIT) : 2~45 ( $\mu \mathrm{s}$ )
$20 \times 4,5 \mathrm{~mm}$ character height (RS stock no. 206-9045)

|  |  | Min. | Max. | Note |
| :---: | :---: | :---: | :---: | :---: |
| (1) | tsu (DATA) | 65 ns | - |  |
| (2) | th (DATA) | 55ns | - |  |
| (3) | tsu ( $\overline{\text { SEL) }}$ | 75ns | - |  |
| (4) | th ( $\overline{\mathrm{SEL}})$ | Ons | - |  |
| (5) | tpw ( $\overline{\mathrm{WR}})$ | 75ns | - |  |
| (6) | twait (1) | Ons | - |  |
| (7) | twait (2) | 200ns | - | For Min. 200ns, $\overline{\mathrm{WR}}$ should not be active (positive H), after BUSY is '0' |
| (8) | tdelay | - | 50ns |  |

SERIAL INPUT

(1) $\mathrm{t}(\mathrm{DATA})=10^{6} / \mathrm{b}$ buud rate $(\mu \mathrm{s})$
(This depends on the selection of the baud rate).
(2) t (DATA) $/ 2(\mu \mathrm{~s})$
(BUSY becomes 'l' at the centre of stop bit).
(3) t (WAIT) : $0(\mu \mathrm{~s})$
(START should be active (' 0 '), after BUSY is ' 0 ')
$20 \times 4$, ll.3mm character height (RS stock no. 2069067)


## Interface to CPUS

Since the modules are designed to be directly connected to the bus line, the interface can be simplified.
Figures 3 and 4 show examples for interfacing to typical CPUS.
Figure 5, figure 6 and figure 7 show examples for interfacing to typical I/O devices.

Figure 3 Connection to Z-80


Figure 4 Connection to 6809


Connection to I/O devices
Figure 5 Connection to Z80A-S10


Figure 6 Connection to Z80-P10


Figure 7 Connection to 8255


## 232-5862

## Operating recommendations

1. Avoid applying excessive shock or vibration beyond the specification for this module.
2. Since VFDs are made of glass material, careful handling is important.
3. Applying lower voltage than specified may cause non activation for the selected pixels.
Conversely, a higher voltage may cause nonselected pixels to become activated. If the above phenomenon is observed, check the voltage level of the power supply.
4. Avoid plugging or unplugging the interface connection with the power on.
5. If the start up time of the supply voltage is slow, the CPU may not be reset. The supply voltage must have risen up to a specified voltage level, within 30 msec .
6. When the power supply is turned on, such as 5A, lmsec. of rush current may flow, because of the dc/dc converter equipped on the module.
7. Avoid using the module where excessive interference of noise is expected. Noise affects the interface signal and causes improper operation. Keep the length of the interface cable less than 50 cm .
8. When the power supply is turned off, the capacitor is not discharged immediately. The high voltage applied to the VFD must not contact the controller IC. Shorting of the mounted components, (within 30sec. after the power is off) may cause damage.
[^2]An Electrocomponents Company


[^0]:    SP = SPACE

[^1]:    SP = SPACE

[^2]:    The information provided in RS technical literature is believed to be accurate and reliable; however, RS Components assumes no responsibility for inaccuracies or omissions, or for the use of this information, and all use of such information shall be entirely at the user's own risk.
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