Low-Power Linear Active ThermistorTM ICs

Features

- · Tiny Analog Temperature Sensor
- Available Packages: SC-70-5, SOT-23-5, TO-92-3
- · Wide Temperature Measurement Range:
 - -40°C to +125°C
- · Accuracy:
 - ±2°C (max.), 0°C to +70°C (MCP9700A/9701A)
 - ±4°C (max.), 0°C to +70°C (MCP9700/9701)
- Optimized for Analog-to-Digital Converters (ADCs):
 - 10.0 mV/°C (typical) MCP9700/9700A
 - 19.5 mV/°C (typical) MCP9701/9701A
- · Wide Operating Voltage Range:
 - V_{DD} = 2.3V to 5.5V MCP9700/9700A
 - V_{DD} = 3.1V to 5.5V MCP9701/9701A
- Low Operating Current: 6 µA (typical)
- · Optimized to Drive Large Capacitive Loads

Typical Applications

- · Hard Disk Drives and Other PC Peripherals
- · Entertainment Systems
- · Home Appliance
- · Office Equipment
- · Battery Packs and Portable Equipment
- · General Purpose Temperature Monitoring

Description

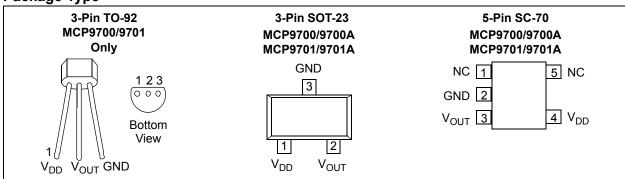
The MCP9700/9700A and MCP9701/9701A family of Linear Active Thermistor TM Intergrated Circuit (IC) is an analog temperature sensor that converts temperature to analog voltage. It's a low-cost, low-power sensor with an accuracy of $\pm 2^{\circ}\text{C}$ from 0°C to $\pm 70^{\circ}\text{C}$ (MCP9700A/9701A) $\pm 4^{\circ}\text{C}$ from 0°C to $\pm 70^{\circ}\text{C}$ (MCP9700/9701) while consuming 6 μA (typical) of operating current.

Unlike resistive sensors (such as thermistors), the Linear Active Thermistor IC does not require an additional signal-conditioning circuit. Therefore, the biasing circuit development overhead for thermistor solutions can be avoided by implementing this low-cost device. The voltage output pin (V_{OUT}) can be directly connected to the ADC input of a microcontroller. The MCP9700/9700A and MCP9701/9701A temperature coefficients are scaled to provide a 1°C/bit resolution for an 8-bit ADC with a reference voltage of 2.5V and 5V, respectively.

The MCP9700/9700A and MCP9701/9701A provide a low-cost solution for applications that require measurement of a relative change of temperature. When measuring relative change in temperature from +25°C, an accuracy of ±1°C (typical) can be realized from 0°C to +70°C. This accuracy can also be achieved by applying system calibration at +25°C.

In addition, this family is immune to the effects of parasitic capacitance and can drive large capacitive loads. This provides Printed Circuit Board (PCB) layout design flexibility by enabling the device to be remotely located from the microcontroller. Adding some capacitance at the output also helps the output transient response by reducing overshoots or undershoots. However, capacitive load is not required for sensor output stability.

Package Type



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

 $V_{DD}: \qquad \qquad \qquad 6.0V$ Storage temperature: $\qquad \qquad -65^{\circ}\text{C to } +150^{\circ}\text{C}$ Ambient Temp. with Power Applied: $\qquad -40^{\circ}\text{C to } +125^{\circ}\text{C}$ Junction Temperature (T_J): $\qquad \qquad \qquad 150^{\circ}\text{C}$ ESD Protection On All Pins (HBM:MM): $\qquad \qquad (4 \text{ kV:} 200V)$ Latch-Up Current at Each Pin: $\qquad \qquad \pm 200 \text{ mA}$

†Notice: Stresses above those listed under "Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS Electrical Specifications: Unless otherwise indicated:

MCP9700/9700A: $V_{DD} = 2.3V$ to 5.5V, GND = Ground, $T_A = -40^{\circ}$ C to +125°C and No load. MCP9701/9701A: $V_{DD} = 3.1 \text{V to } 5.5 \text{V}$, GND = Ground, $T_A = -10 ^{\circ} \text{C}$ to $+125 ^{\circ} \text{C}$ and No load. **Conditions Parameter** Sym Min Typ Max Unit **Power Supply** $V_{DD} \\$ V MCP9700/9700A Operating Voltage Range 2.3 5.5 3.1 5.5 V MCP9701/9701A V_{DD} **Operating Current** 6 12 μΑ I_{DD} Power Supply Rejection Δ °C/ Δ V_{DD} 0.1 °C/V Sensor Accuracy (Notes 1, 2) $T_A = +25$ °C °C T_{ACY} ±1 $T_{\Delta} = 0^{\circ}C \text{ to } +70^{\circ}C$ °C TACY -2.0 ±1 +2.0 MCP9700A/9701A $T_A = -40^{\circ}C \text{ to } +125^{\circ}C$ -2.0 +4.0 °C MCP9700A T_{ACY} ±1 °C $T_A = -10^{\circ}C \text{ to } +125^{\circ}C$ -2.0 +4.0 MCP9701A T_{ACY} ±1 $T_A = 0^{\circ}C$ to $+70^{\circ}C$ -4.0 ±2 +4.0 °C MCP9700/9701 T_{ACY} $T_A = -40^{\circ}C \text{ to } +125^{\circ}C$ -4.0 +2 +6.0 °C MCP9700 T_{ACY} $T_A = -10^{\circ}C \text{ to } +125^{\circ}C$ °C -4.0 ±2 +6.0 MCP9701 T_{ACY} **Sensor Output**

V_{0°C}

 $V_{0^{\circ}C}$

 T_{C}

 T_{C}

 V_{ONL}

 I_{OUT}

Z_{OUT}

 ΔV_{OUT}

 ΔI_{OUT}

Note 1:	The MCP9700/9700A fai	mily accurac	v ic toctod	with \/	- 2 3\/ wh	ila tha MCE	00701/07011 0	couracy ic
MOLE I.	THE MICE STOURSTOUR IA	illy accurac	y is lesieu	MILLI A DD	– 3.3 v, wii	HE THE MICE	'9101/9101A a	ccuracy is
		-	-					-
	tested with $V_{DD} = 5.0V$.							
	100104 11111 1 00 01011							

2: The MCP9700/9700A and MCP9701/9701A family is characterized using the first-order or linear equation, as shown in Equation 4-2.

500

400

10.0

19.5

±0.5

20

1

mV

mV

mV/°C

mV/°C

°C

μΑ

Ω

Ω

100

MCP9700/9700A

MCP9701/9701A

MCP9700/9700A

MCP9701/9701A

 $I_{OUT} = 100 \mu A$

 $T_A = 0^{\circ}C \text{ to } +70^{\circ}C \text{ (Note 2)}$

 I_{OUT} = 100 μ A, f = 500 Hz T_A = 0°C to +70°C,

- **3:** The MCP9700/9700A and MCP9701/9701A family is characterized and production tested with a capacitive load of 1000 pF.
- **4:** SC-70-5 package thermal response with 1x1 inch, dual-sided copper clad, TO-92-3 package thermal response without PCB (leaded).

Output Voltage, $T_A = 0$ °C

Output Voltage, $T_A = 0$ °C

Temperature Coefficient

Output Non-linearity

Output Impedance

Output Load Regulation

Output Current

DC ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Specifications: Unless otherwise indicated:

MCP9700/9700A: V_{DD} = 2.3V to 5.5V, GND = Ground, T_A = -40°C to +125°C and No load. **MCP9701/9701A:** V_{DD} = 3.1V to 5.5V, GND = Ground, T_A = -10°C to +125°C and No load.

DD.	•		٦.			
Parameter	Sym	Min	Тур	Max	Unit	Conditions
Turn-on Time	t _{ON}	_	800	_	μs	
Typical Load Capacitance (Note 3)	C _{LOAD}	_	_	1000	pF	
SC-70 Thermal Response to 63%	t _{RES}	_	1.3	_	S	30°C (Air) to +125°C
TO-92 Thermal Response to 63%	t _{RES}	_	1.65	_	s	(Fluid Bath) (Note 4)

- Note 1: The MCP9700/9700A family accuracy is tested with V_{DD} = 3.3V, while the MCP9701/9701A accuracy is tested with V_{DD} = 5.0V.
 - 2: The MCP9700/9700A and MCP9701/9701A family is characterized using the first-order or linear equation, as shown in Equation 4-2.
 - **3:** The MCP9700/9700A and MCP9701/9701A family is characterized and production tested with a capacitive load of 1000 pF.
 - **4:** SC-70-5 package thermal response with 1x1 inch, dual-sided copper clad, TO-92-3 package thermal response without PCB (leaded).

TEMPERATURE CHARACTERISTICS

Electrical Specifications: Unless otherwise indicated:

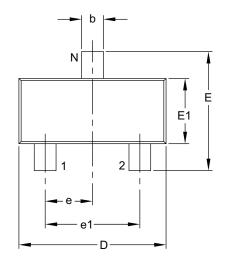
MCP07001070001. V = 2.3 V/to 5.5 V CND = Cround T = 40°C to +125°C

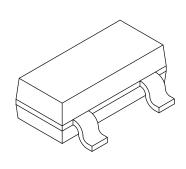
MCP9700/9700A: V_{DD} = 2.3V to 5.5V, GND = Ground, T_A = -40°C to +125°C and No load. **MCP9701/9701A:** V_{DD} = 3.1V to 5.5V, GND = Ground, T_A = -10°C to +125°C and No load.

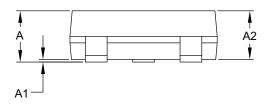
MCP9701/9701A: $V_{DD} = 3.1V$ to 5.5V, GND = Ground, $T_A = -10^{\circ}$ C to +125°C and No load.								
Parameters	Sym	Min	Тур	Max	Units	Conditions		
Temperature Ranges								
Specified Temperature Range	T _A	-40	_	+125	°C	MCP9700/9700A (Note)		
	T _A	-10	_	+125	°C	MCP9701/9701A (Note)		
Operating Temperature Range	T_A	-40	_	+125	°C			
Storage Temperature Range	T_A	-65	_	+150	°C			
Thermal Package Resistances								
Thermal Resistance, 5LD SC-70	θ_{JA}	_	331	ı	°C/W			
Thermal Resistance, 3LD SOT-23	θ_{JA}	_	336	_	°C/W			
Thermal Resistance, 3LD TO-92	$\theta_{\sf JA}$	_	131.9	_	°C/W			

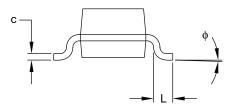
Note: Operation in this range must not cause T_J to exceed Maximum Junction Temperature (+150°C).

3-Lead Plastic Small Outline Transistor (TT) [SOT-23]









		MILLIMETERS MIN NOM MAX					
Dimensio	Dimension Limits		NOM	MAX			
Number of Pins	Number of Pins N			3			
Lead Pitch	е	0.95 BSC					
Outside Lead Pitch		1.90 BSC					
Overall Height	Α	0.89 – 1.12					
Molded Package Thickness	A2	0.79	0.95	1.02			
Standoff	A1	0.01	_	0.10			
Overall Width	E	2.10	_	2.64			
Molded Package Width	E1	1.16	1.30	1.40			
Overall Length	D	2.67	2.90	3.05			
Foot Length	L	0.13	0.50	0.60			
Foot Angle	ф	0°	_	10°			
Lead Thickness		0.08	_	0.20			
Lead Width		0.30	_	0.54			

Notes:

- 1. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.25 mm per side.
- 2. Dimensioning and tolerancing per ASME Y14.5M.

BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-104B

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

PART NO. – X /XX			Examples:			
	erature Package nge	a) b)	MCP9700T-E/LT: MCP9700-E/TO:	Linear Active Thermistor™ IC, Tape and Reel, 5LD SC-70 package. Linear Active Thermistor™		
Device:	MCP9700T: Linear Active Thermistor™ IC, Tape and Reel, Pb free MCP9700AT: Linear Active Thermistor™ IC, Tape and Reel, Pb free MCP9701T: Linear Active Thermistor™ IC, Tape and Reel, Pb free MCP9701AT: Linear Active Thermistor™ IC, Tape and Reel, Pb free	c) d) e)	MCP9700AT-E/LT: MCP9700AT-E/LT:	IC, 3LD TO-92 package. Linear Active Thermistor™ IC, Tape and Reel, 3LD SOT-23 package. Linear Active Thermistor™ IC, Tape and Reel, 5LD SC-70 package.		
Temperature Range:	E = -40°C to +125°C	a)	MCP9701T-E/LT:	Linear Active Thermistor™ IC, Tape and Reel,		
Package:	LT = Plastic Small Outline Transistor, 5-lead TO = Plastic Small Outline Transistor, 3-lead TT = Plastic Small Outline Transistor, 3-lead	b)c)d)e)	MCP9701-E/TO: MCP9701T-E/OT: MCP9701AT-E/LT: MCP9701AT-E/OT:	5LD SC-70 package. Linear Active Thermistor™ IC, 3LD TO-92 package. Linear Active Thermistor™ IC, Tape and Reel, 3LD SOT-23 package. Linear Active Thermistor™ IC, Tape and Reel, 5LD SC-70 package. Linear Active Thermistor™ IC, Tape and Reel, 3LD SOT-23 package.		