

N-Channel 20-V (D-S) MOSFET

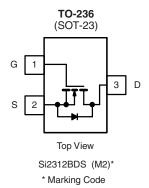
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)	
	0.031 at V _{GS} = 4.5 V	5.0		
20	0.037 at V _{GS} = 2.5 V	4.6	7.5	
	0.047 at V _{GS} = 1.8 V	4.1		

FEATURES

- Halogen-free Option Available
 TrenchFET[®] Power MOSFET
- 100 % R_g Tested



COMPLIANT



Ordering Information: Si2312BDS-T1-E3 (Lead (Pb)-free)

Si2312BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	Γ _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	20		V
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current /T 150 °C)	T _A = 25 °C	I _D	5.0	3.9	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		4.0	3.1	
Pulsed Drain Current ^b		I _{DM}	15		Α
Avalanche Current ^b	L = 0.1 mH	I _{AS}	13		
Single Avalanche Energy	L = 0.1 IIII1	E _{AS}	8.	45	mJ
Continuous Source Current (Diode Conduction) ^a		I _S	1.0	0.63	Α
Daniel Discission and	T _A = 25 °C	P _D	1.25	0.75	w
Power Dissipation ^a	T _A = 70 °C		0.80	0.48	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manifestor Location to Applicated	t ≤ 5 s	R _{thJA}	80	100	°C/W
Maximum Junction-to-Ambient ^a	Steady State		120	166	
Maximum Junction-to-Foot	Steady State	R_{thJF}	50	60	

- a. Surface Mounted on 1" x 1" FR4 board.
- b. Pulse width limited by maximum junction temperature.

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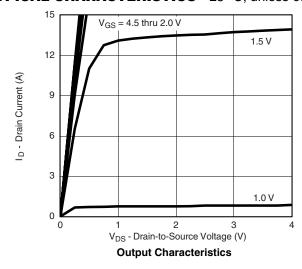
	Symbol		Limits				
Parameter		Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			٧	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	' _{GS} , I _D = 250 μA 0.45		0.85]	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zana Oata Wallana Busin Our	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V			1	μΑ	
Zero Gate Voltage Drain Current		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 70 °C			75		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 4.5 \text{ V}$	15			Α	
		$V_{GS} = 4.5 \text{ V}, I_D = 5.0 \text{ A}$		0.025	0.031		
Drain-Source On-Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 4.6 \text{ A}$		0.030	0.037	Ω	
	Ī	$V_{GS} = 1.8 \text{ V}, I_D = 4.1 \text{ A}$		0.036	0.047		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 5.0 \text{ A}$		30		S	
Diode Forward Voltage	V_{SD}	I _S = 1.0 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			7.5	12		
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5.0 \text{ A}$		1.4		nC	
Gate-Drain Charge	Q_{gd}			1.2			
Gate Resistance	R_g	f = 1.0 MHz	1.1	2.2	3.3	Ω	
Switching							
Turn-On Delay Time	t _{d(on)}			9	15		
Rise Time	t _r	V_{DD} = 10 V, R_L = 10 Ω		30	45		
Turn-Off Delay Time	t _{d(off)}	${ m I_D}\cong$ 1.0 A, ${ m V_{GEN}}$ = 4.5 V, ${ m R_g}$ = 6 ${ m \Omega}$		35	55	ns	
Fall Time	t _f			10	15		
Source-Drain Reverse Recovery Time	t _{rr}	L = 1.0 A di/dt = 100 A/vo		13	25		
Body Diode Reverse Recovery Charge	Q _{rr}	$I_F = 1.0 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		4.5	7	nC	

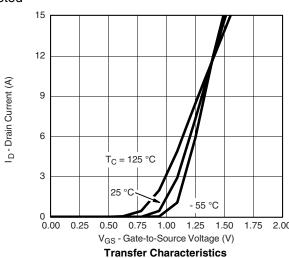
Notes:

- a. Pulse test: Pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



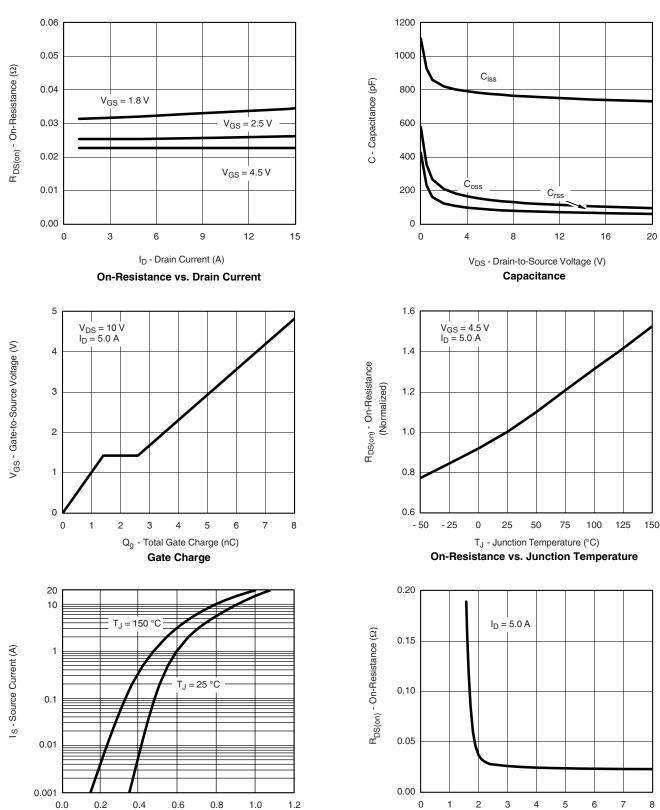








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V_{SD} - Source-to-Drain Voltage (V) **Source-Drain Diode Forward Voltage**

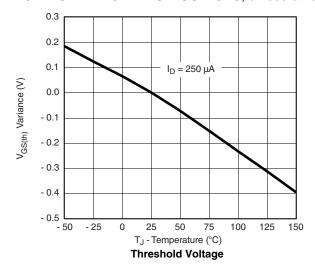
V_{GS} - Gate-to-Source Voltage (V)

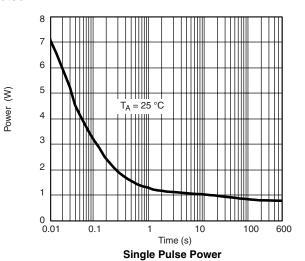
On-Resistance vs. Gate-to-Source Voltage

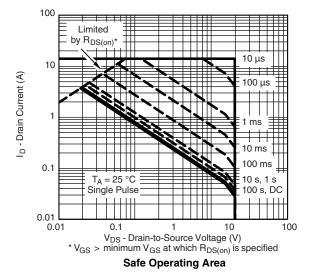
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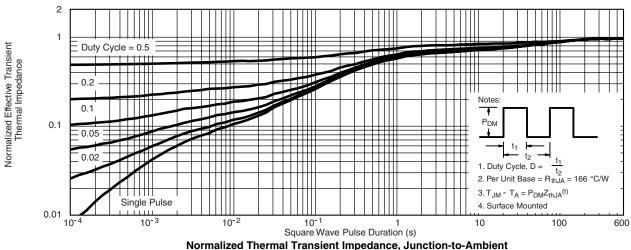
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted









Normalized Thermal Transient Impedance, Junction-to-Ambien

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